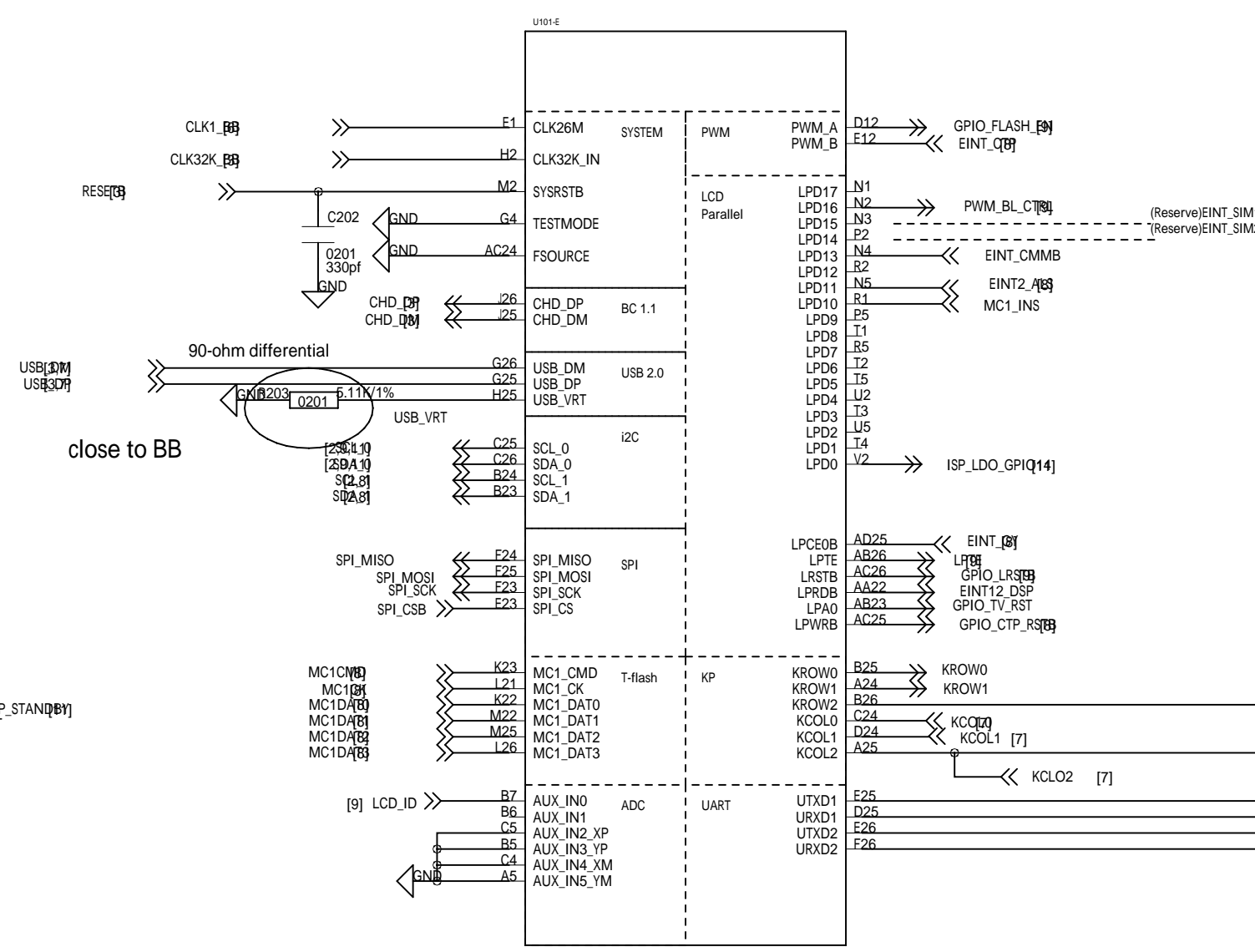
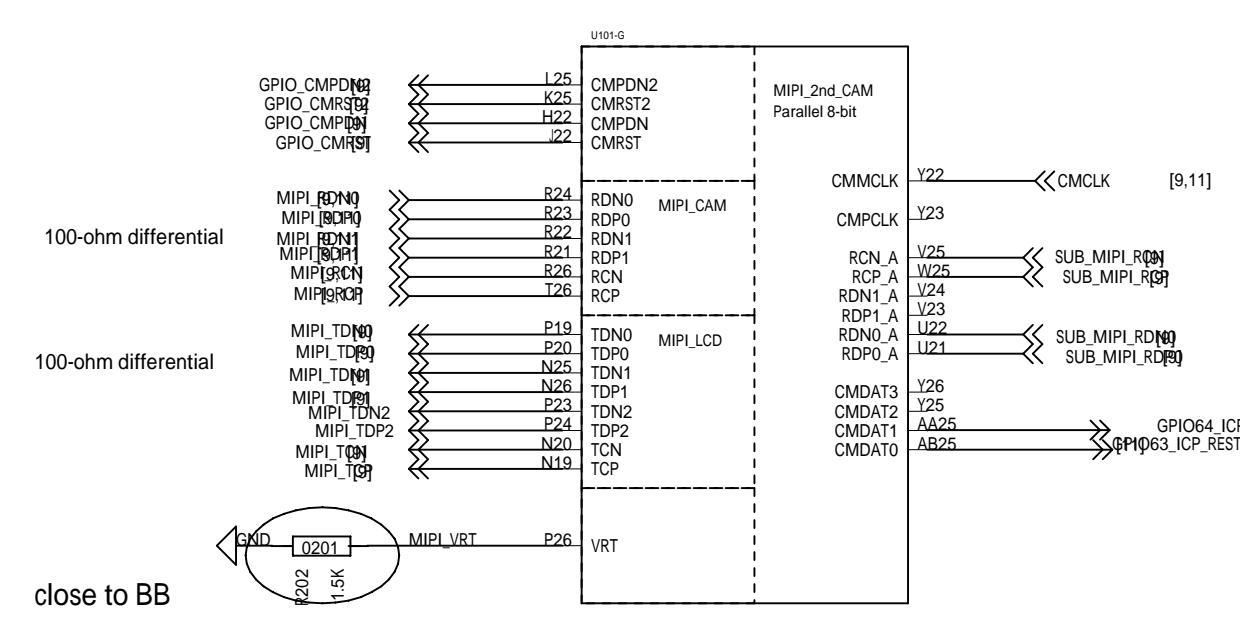
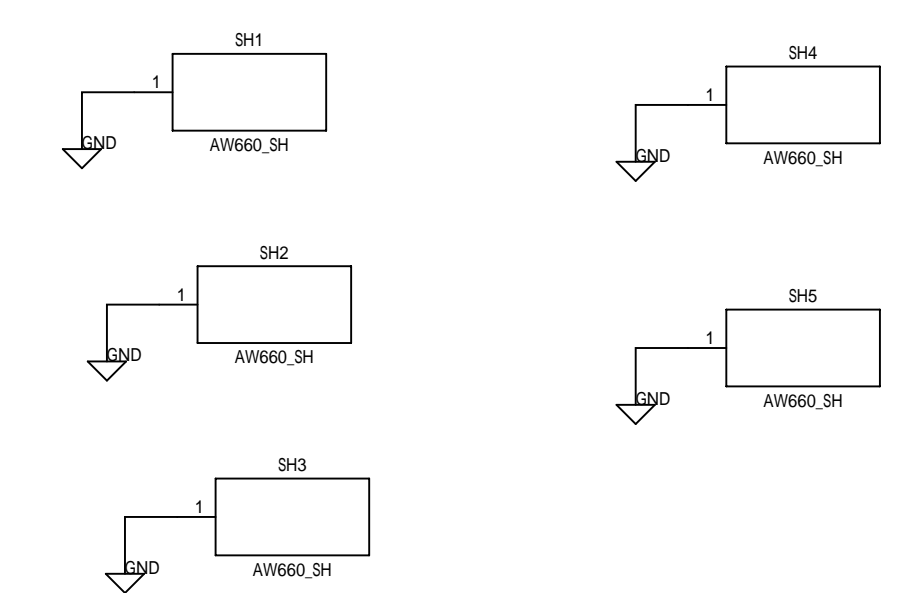
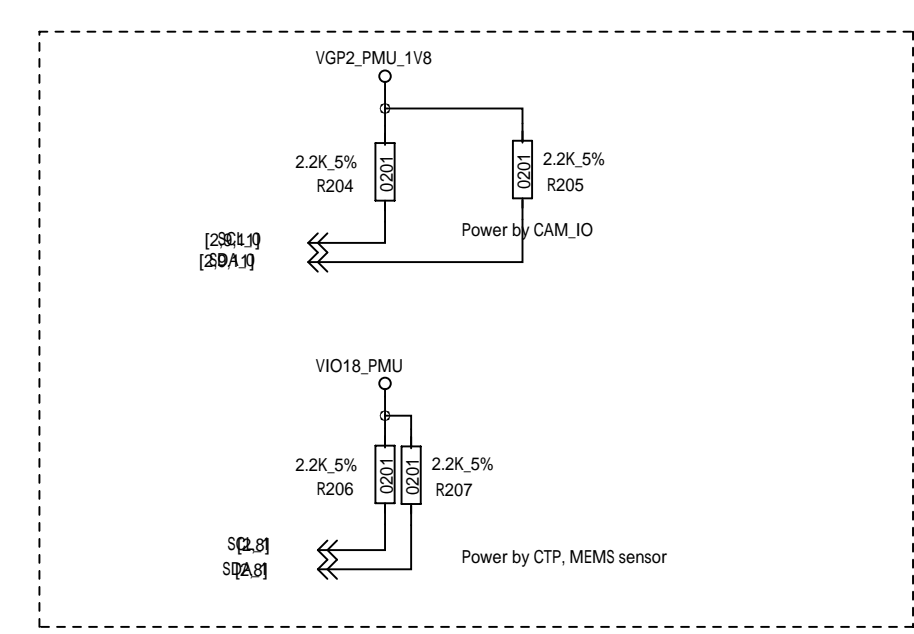


Based on your system level design, if better desense performance is needed on your system, please refer to desense performance enhance proposal

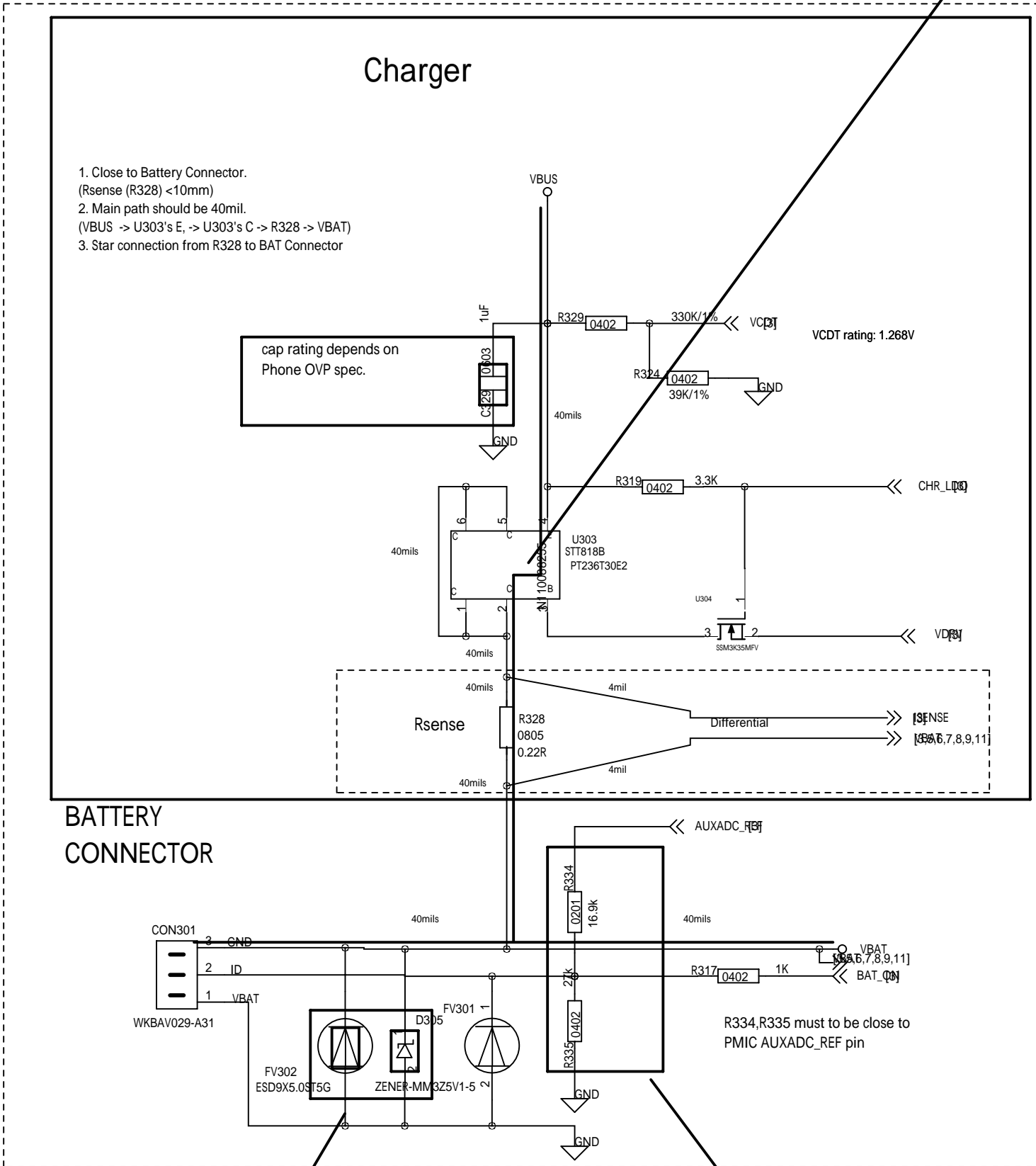


MT6572 support .ITAG from below :
 1. KP (recommend)
 2. MC1
 3. CAM
 for .ITAG pin out from MC1/CAM, refer to HW design notice



BB - peripheral		Rev
MT6572 REF PHONE		V1.0
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Before you select BJT, please take power dissipation into consideration.
Refer to MT6323 design notice



Based on your system level design, if better ESD performance is needed on your system, please refer to ESD performance enhance proposal

If battery NTC is 10kOhm, R334~39K, R335~90K
if battery NTC is 47kOhm, R334~190K, R335~390K
Refer to MT6323 HW design notice

Add Zener Diode
Place on the path from VBAT to IC (Battery connector or test point on ID connector)

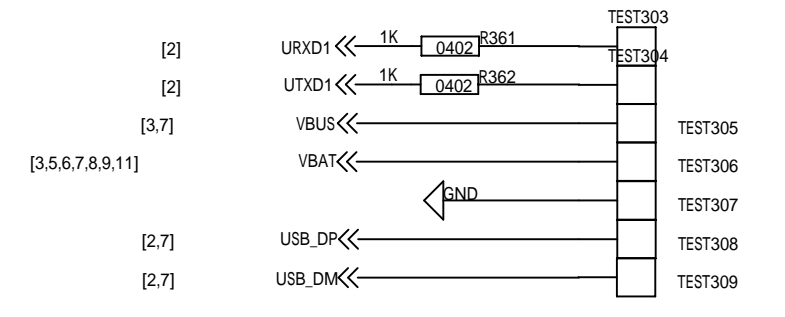
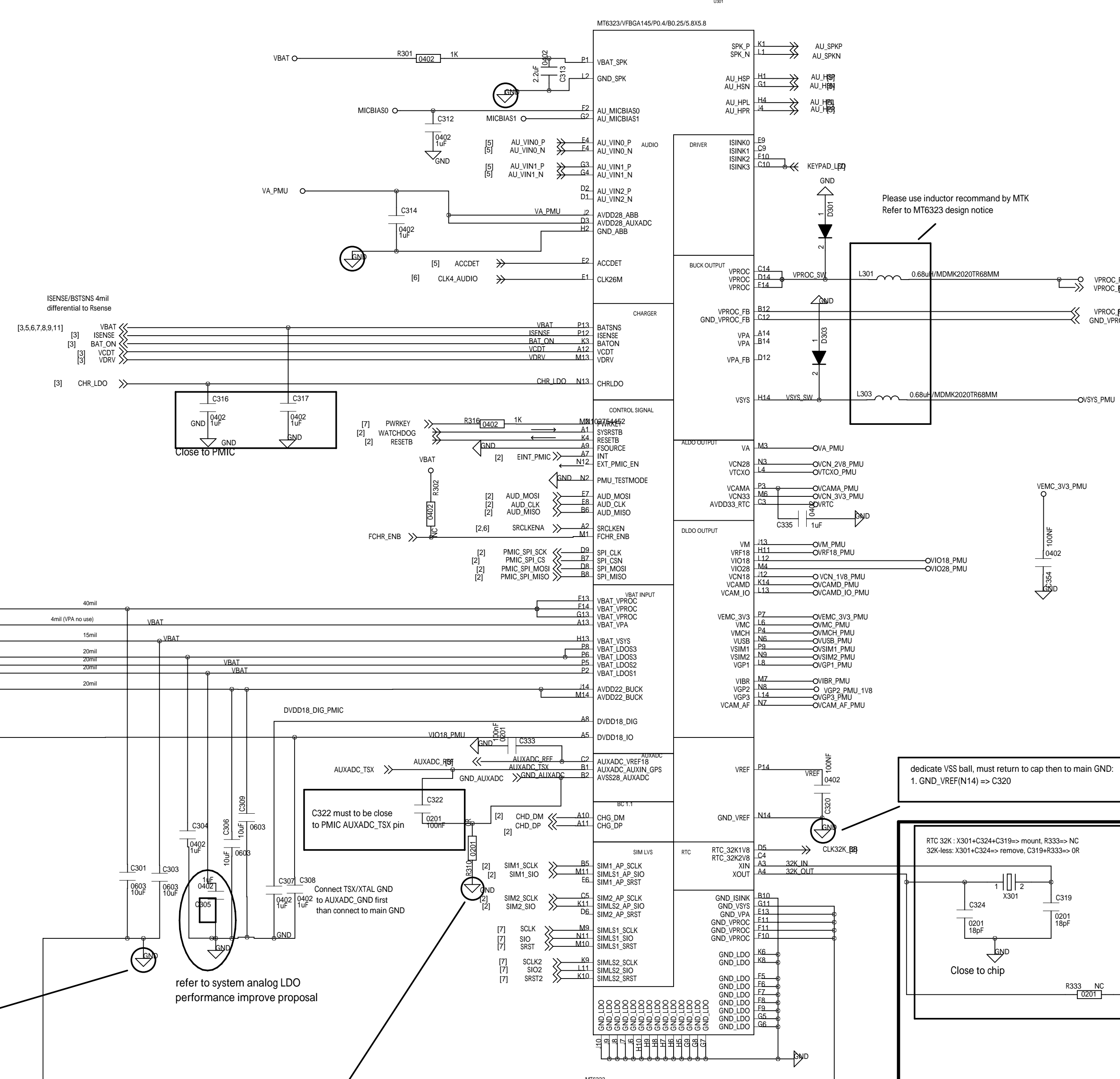
Between IC and IO port

Refer to MT6323 design notice for Zener selection

Based on your system level design, if better EOS performance is needed on your system, please refer to EOS performance enhance proposal

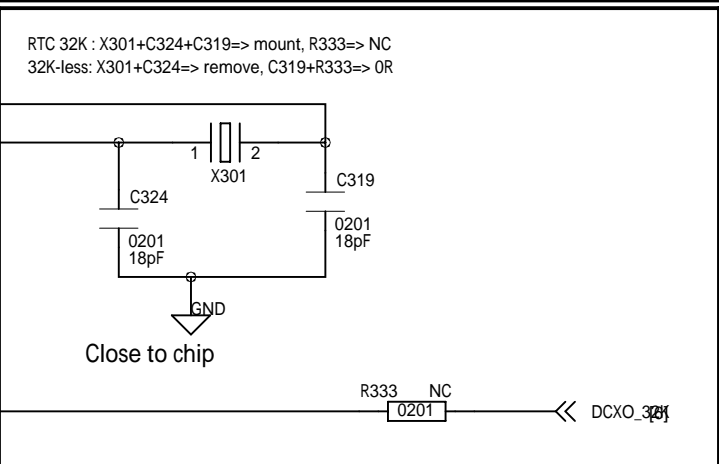
Refer to MT6323 design notice for Buck GND layout rule

Refer to GPS co-clock layout rule



Please use inductor recommend by MTK
Refer to MT6323 design notice

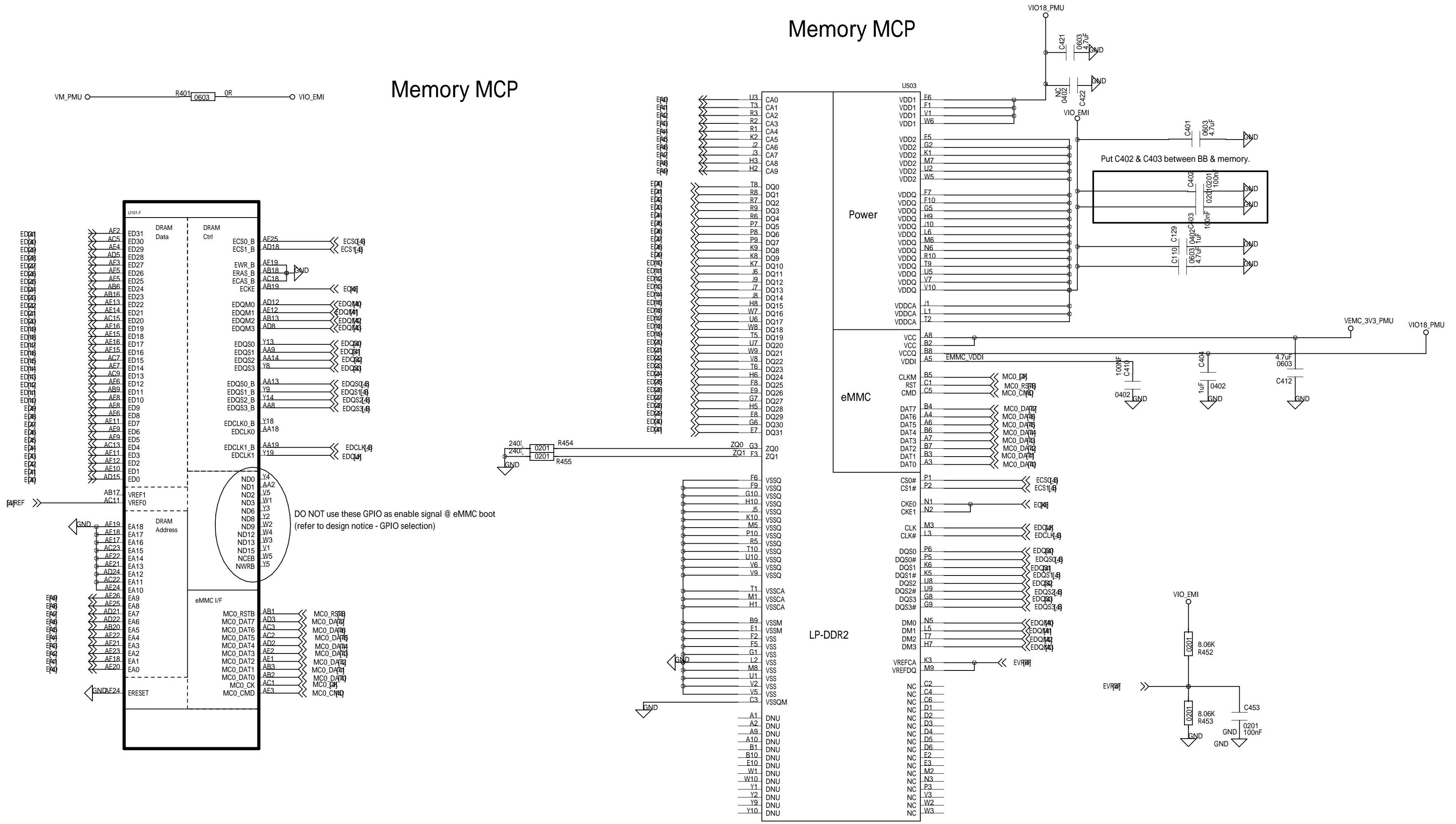
dedicate VSS ball, must return to cap then to main GND.
1. GND_VREF(N14) => C320



=> for longer RTC time sustain after battery remove, please refer to RTC design notice

REV	PMIC	REV
ID	Document Number	REV
MT6572 REF PHONE		V1.0
DATE	Friday, December 28, 2012	PAGE 3 of 99

HW trapping PIN
 20K: VM=1.8V
 NC : VM=1.2V



Memory MCP

Memory MCP

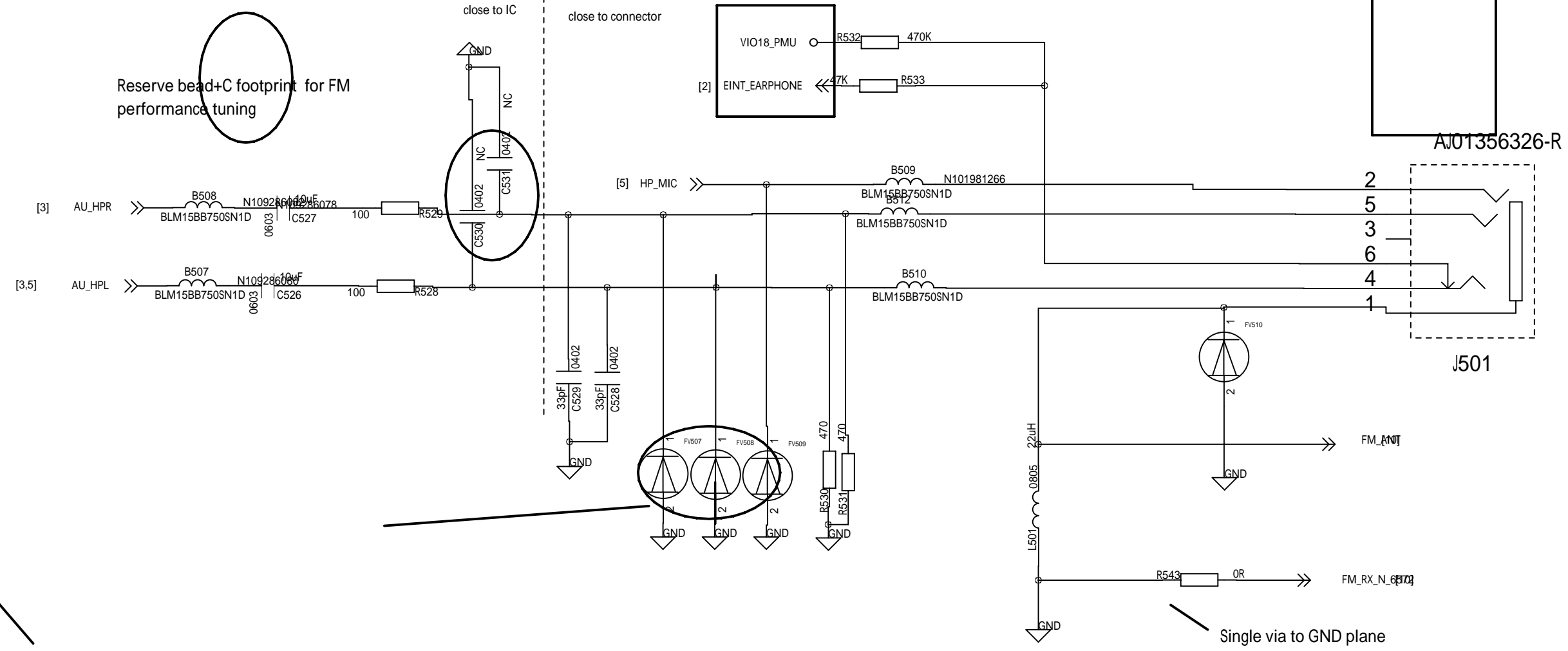
Power

eMMC

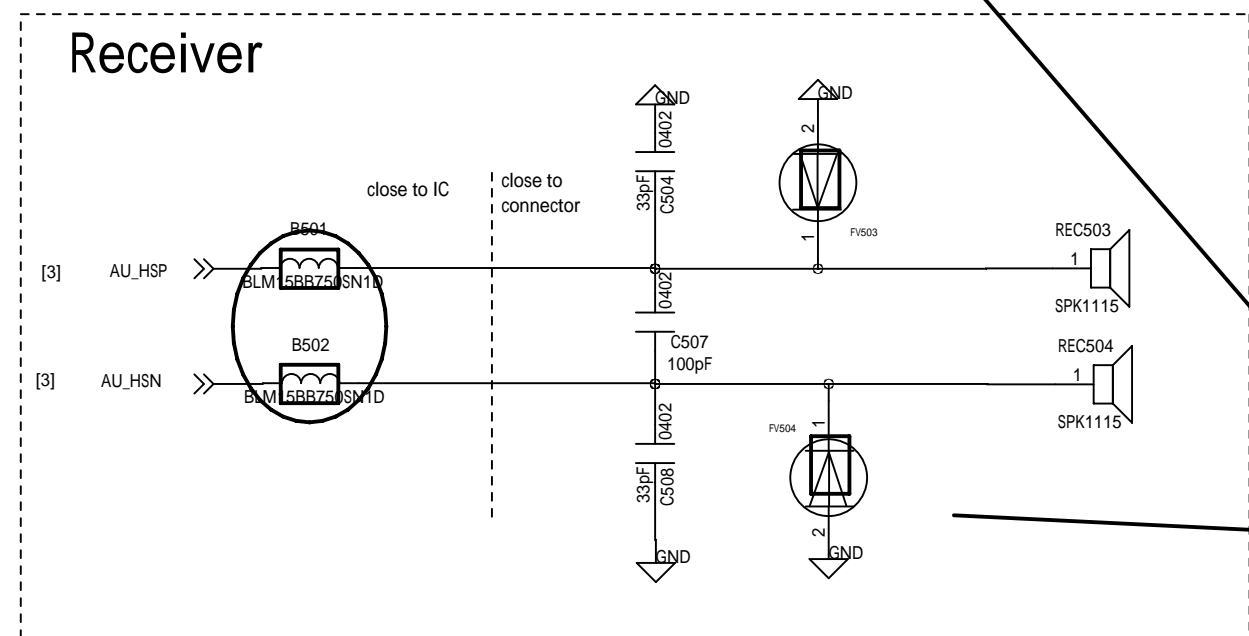
LP-DDR2

H9TP9240MCP.RDM

Earphone Audio

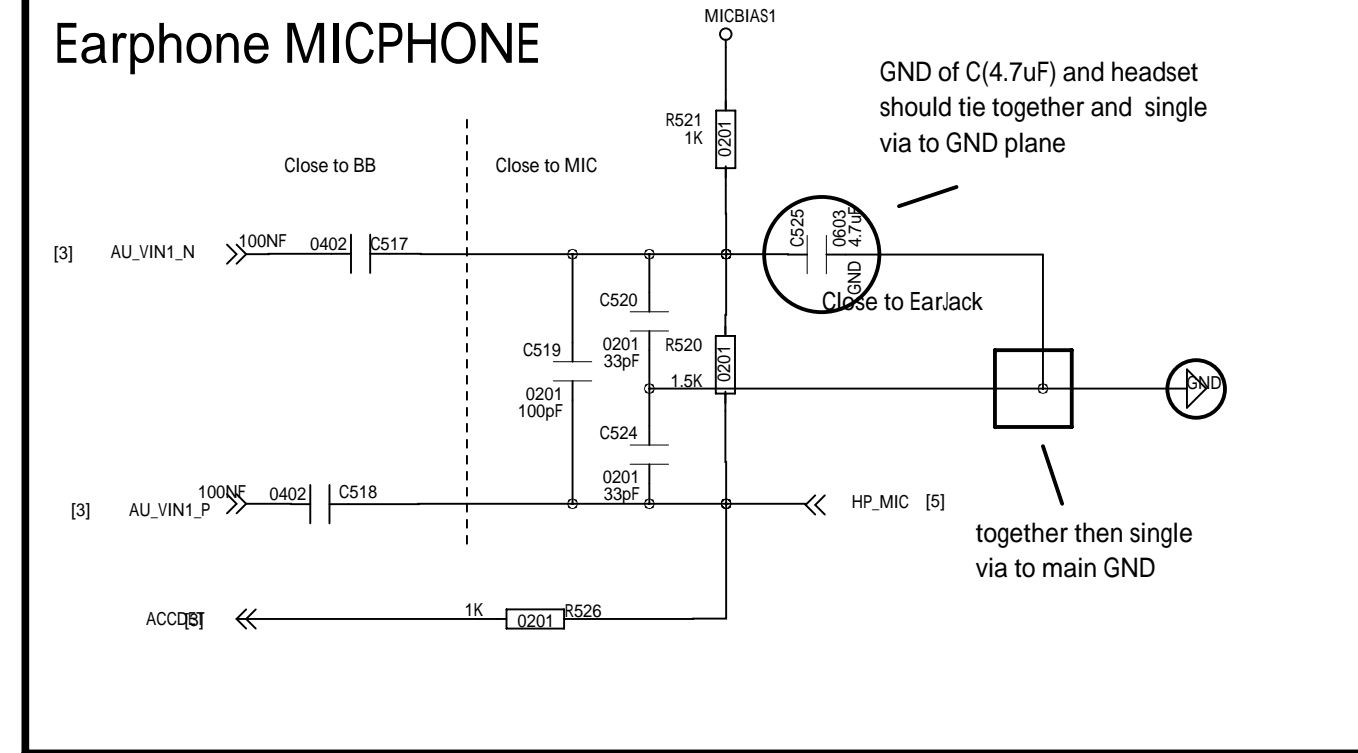


Receiver

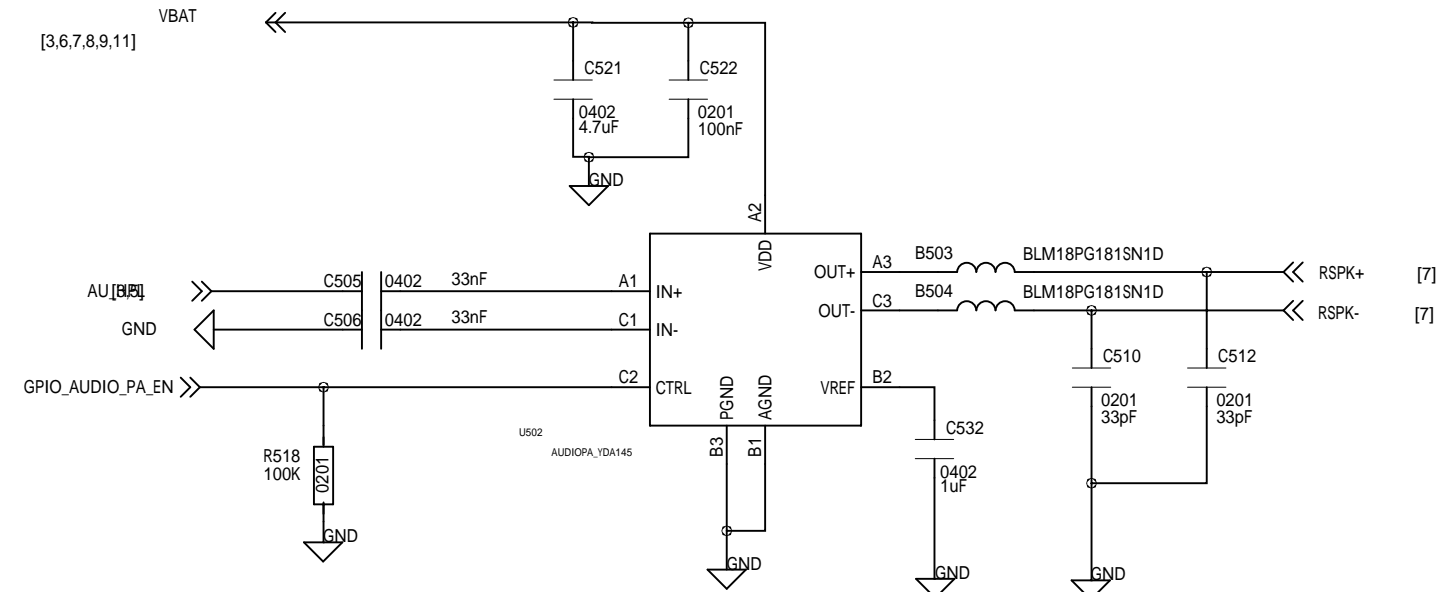
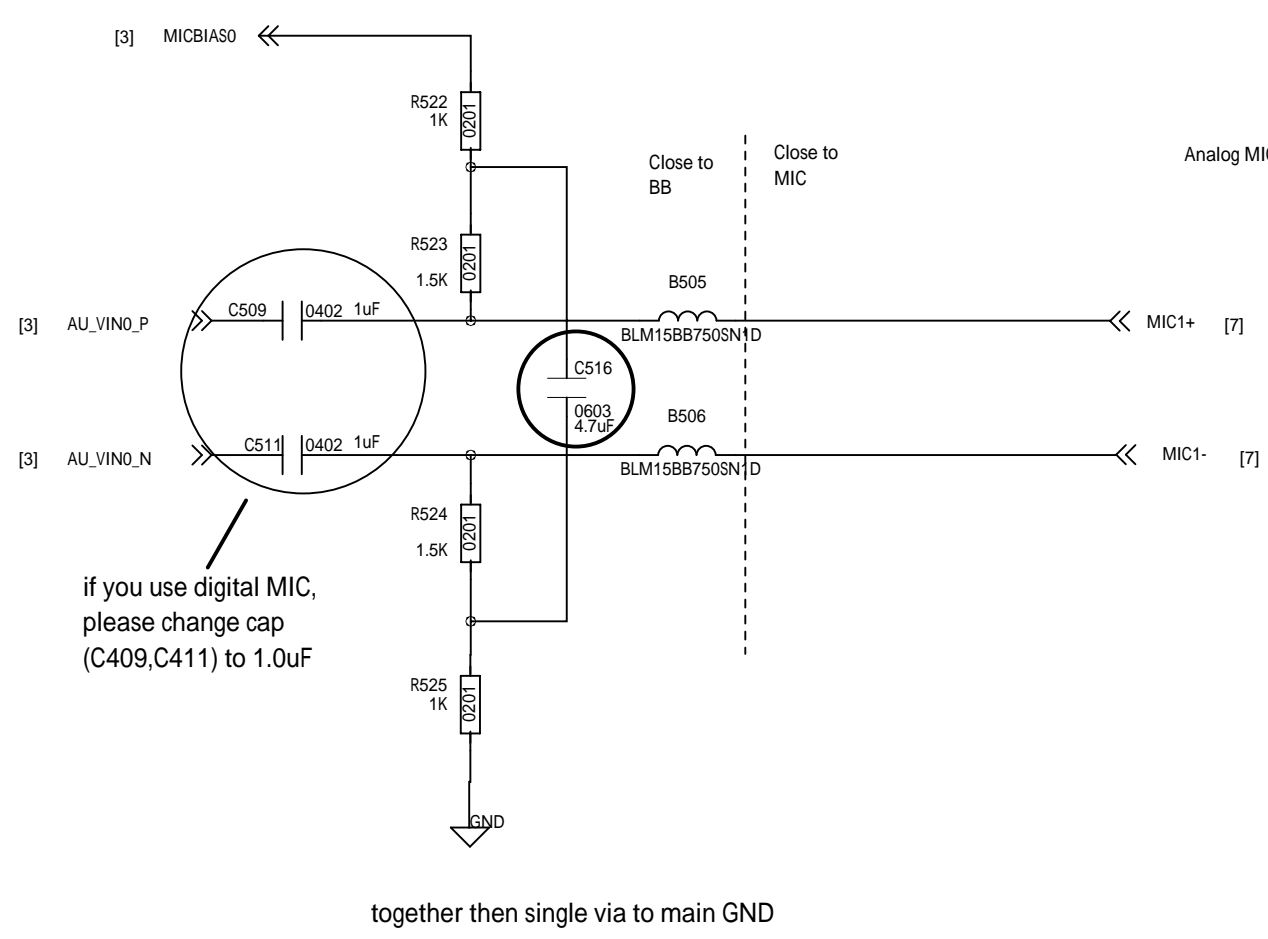


Based on your system level design, if better ESD performance is needed on your system, please refer to ESD performance enhance proposal

Earphone MICPHONE



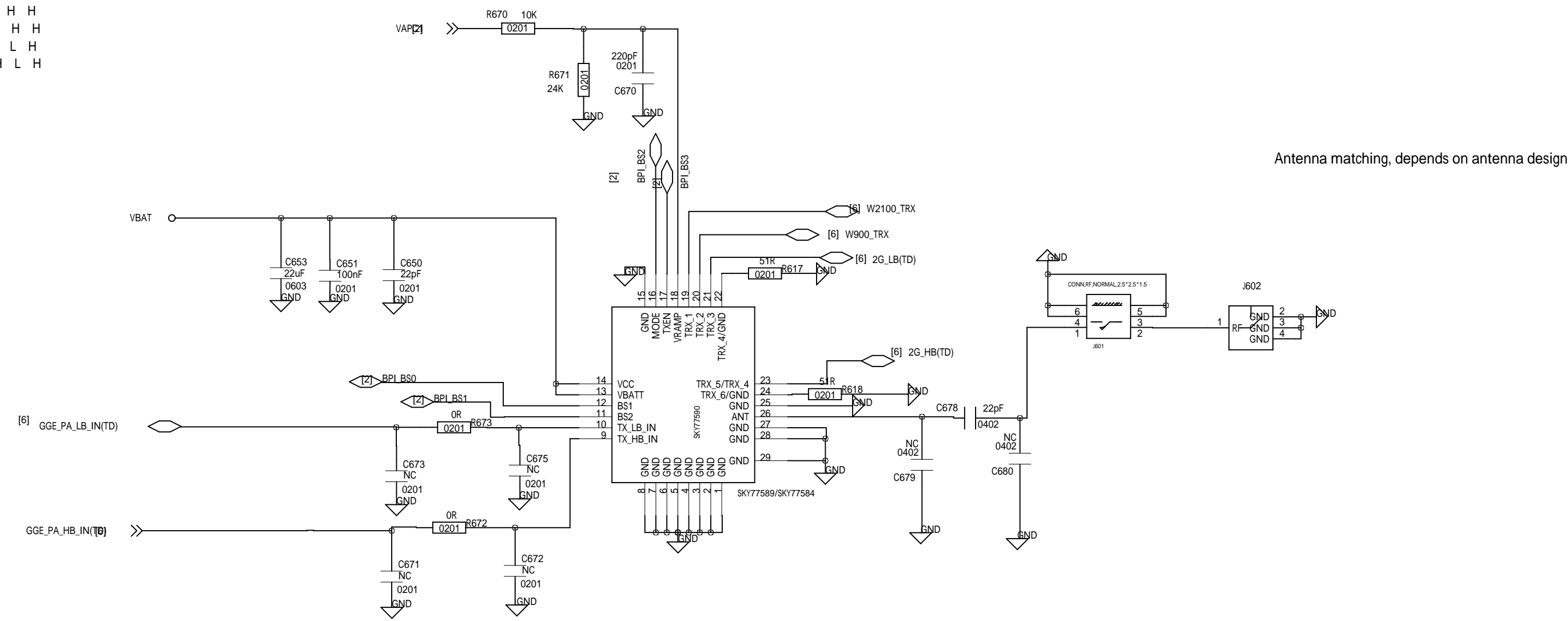
Handset Microphone 1



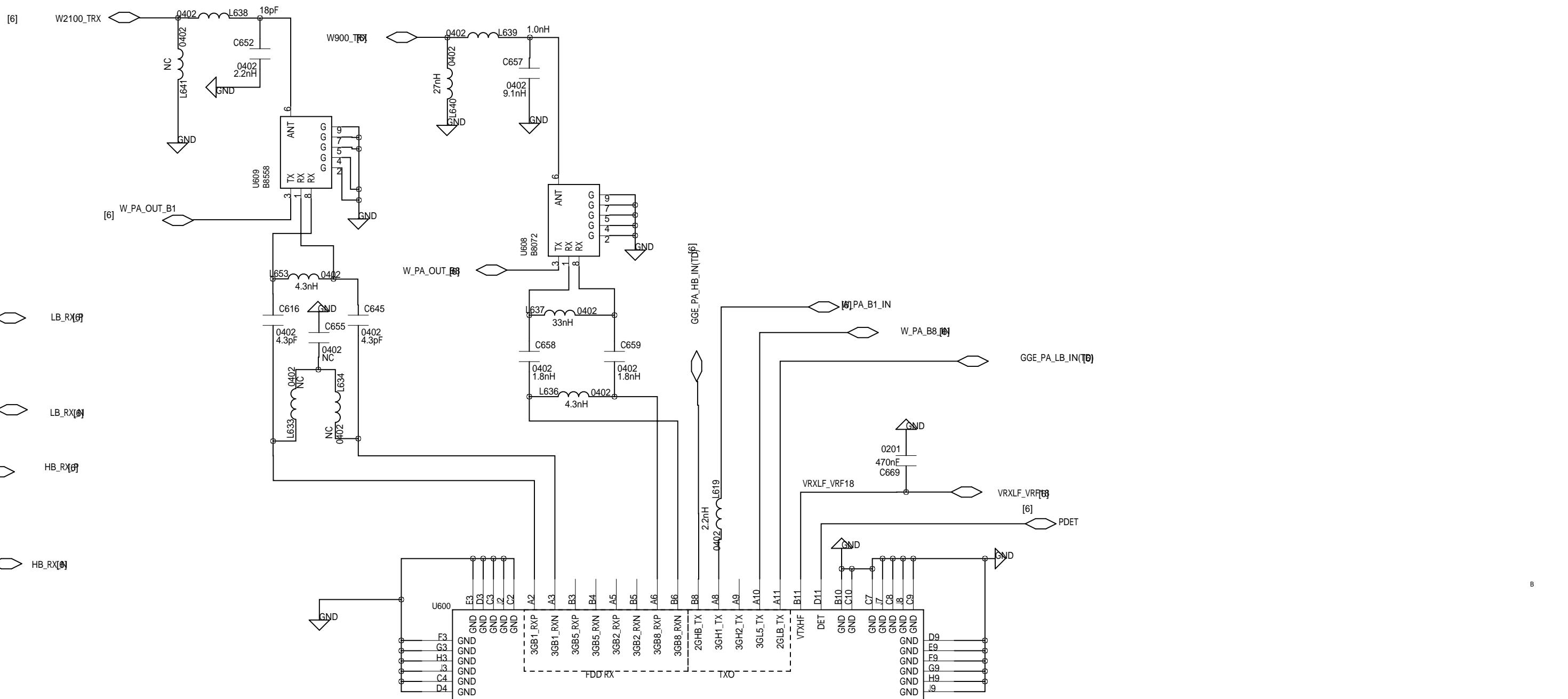
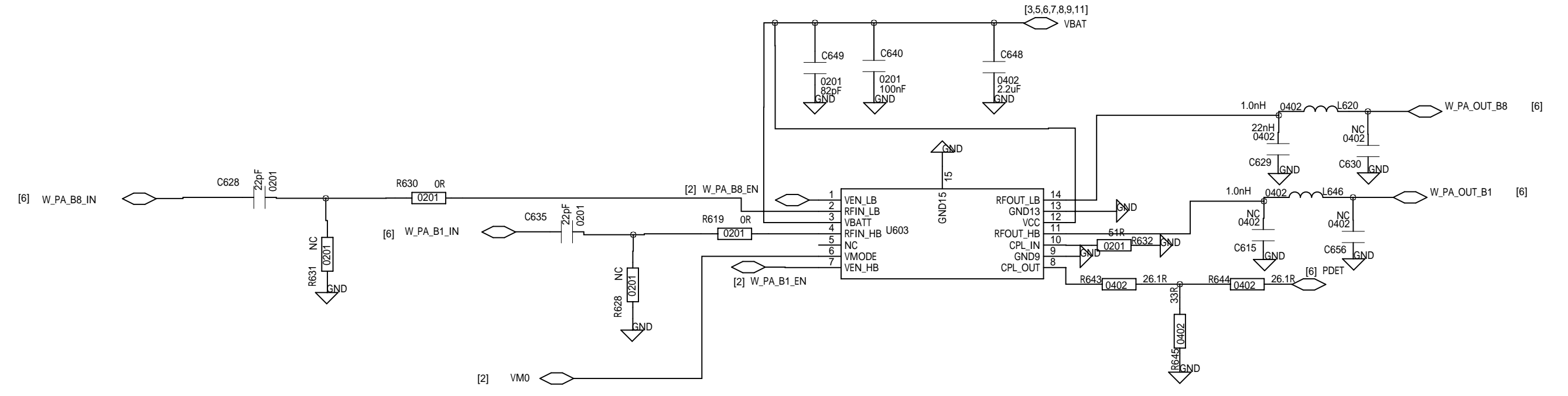
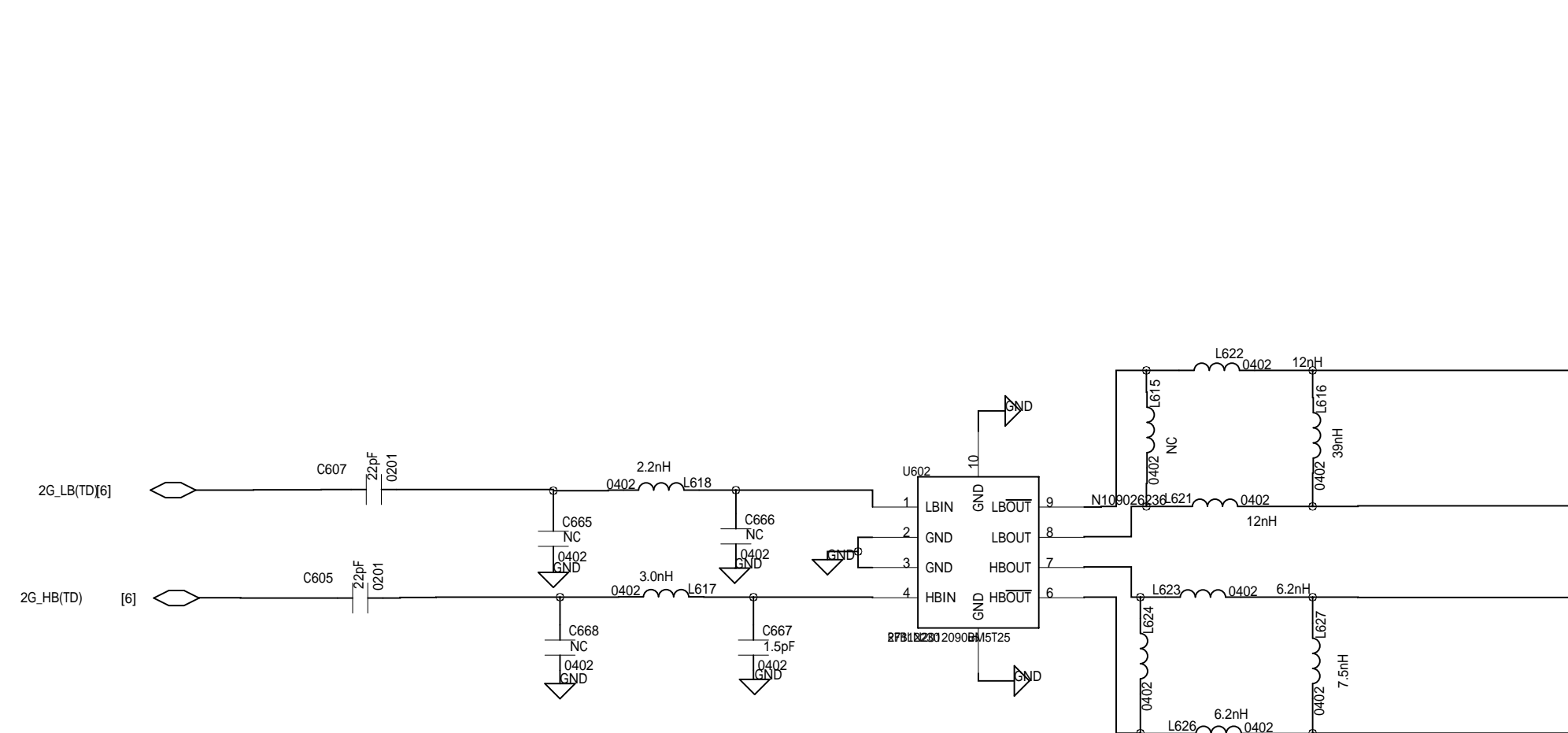
NOTE:

BP10-6 are 2G+3G mode both
 BP17-13 are 3G mode only
 SKY77590 control logic table
 VcA VcB VcC TxEn
 BS2 BS1 mode enable
 (TRX1)G_DCS L L H L
 (TRX2)W_Band8 L H H L
 (TRX3)W_Band1 H L H L
 (TRX4)W_Band5 H H H L
 (TRX5)W_Band2 L H L L
 (TRX6)NA H L L L
 EDGE_TX_H L L H H
 EDGE_TX_H H H H H
 GMSK_TX_L H L L H
 GMSK_TX_H H H L H

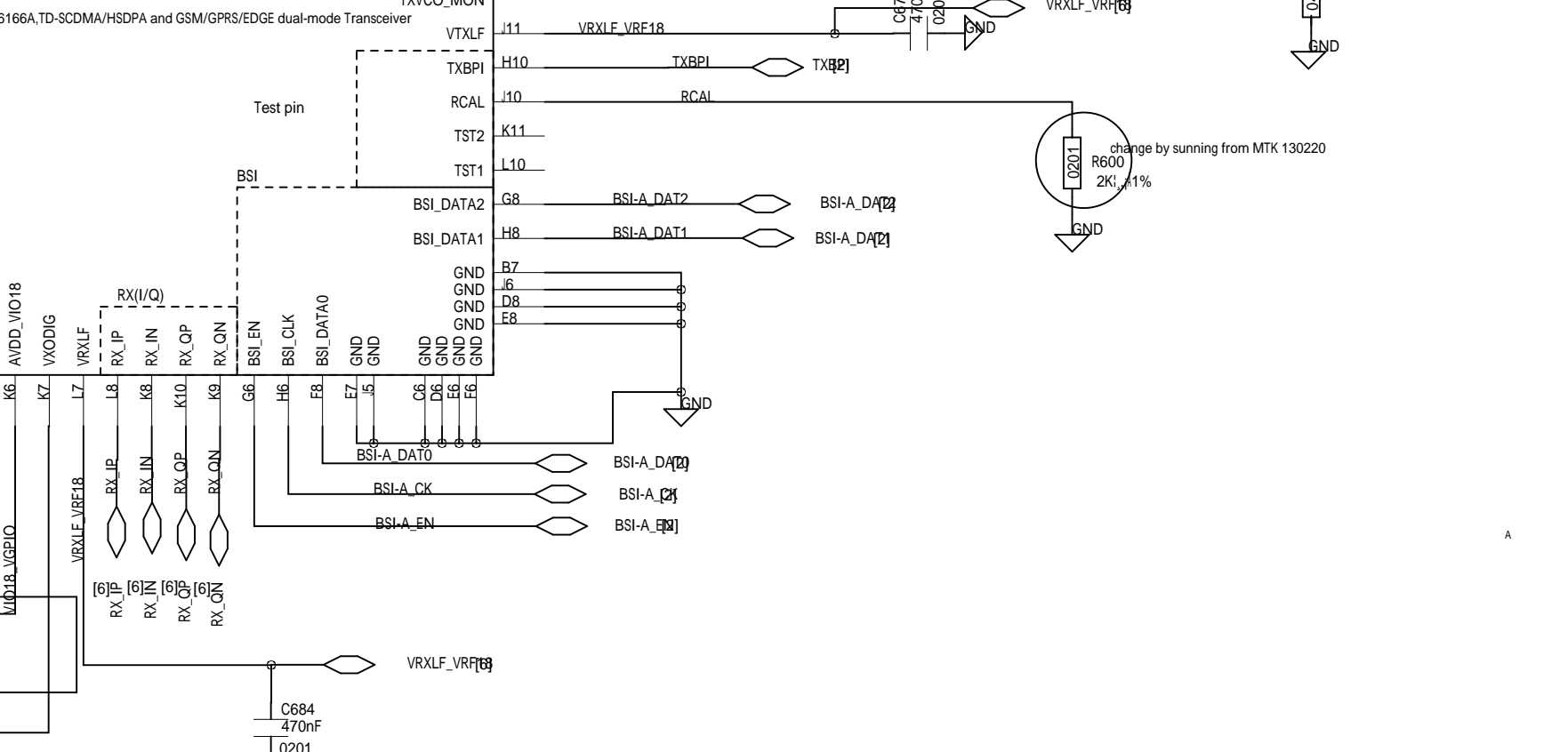
BP10-4 and 10-11 are 2G+3G mode both
 BP15-9 and 12-14 are 3G mode only
 (suggest BP15-9 = 1.8V)



Antenna matching, depends on antenna design



MT1666



Two Application Circuit Conditions,
 1.TSX Circuit : X600=TSX, R653=R656=NC, R654=100K+1%, R655=R657=0ohm
 2.XTAL Circuit : X600=Mobile XTAL, R653=R656=0ohm, R654=R655=R657=NC

Route AUXADC_REF with 4mil trace width

Route AUXADC_TSX with 4mil trace width

Route AUXADC_GND with 24mil trace width under AUXADC_REF/AUXADC_TSX trace

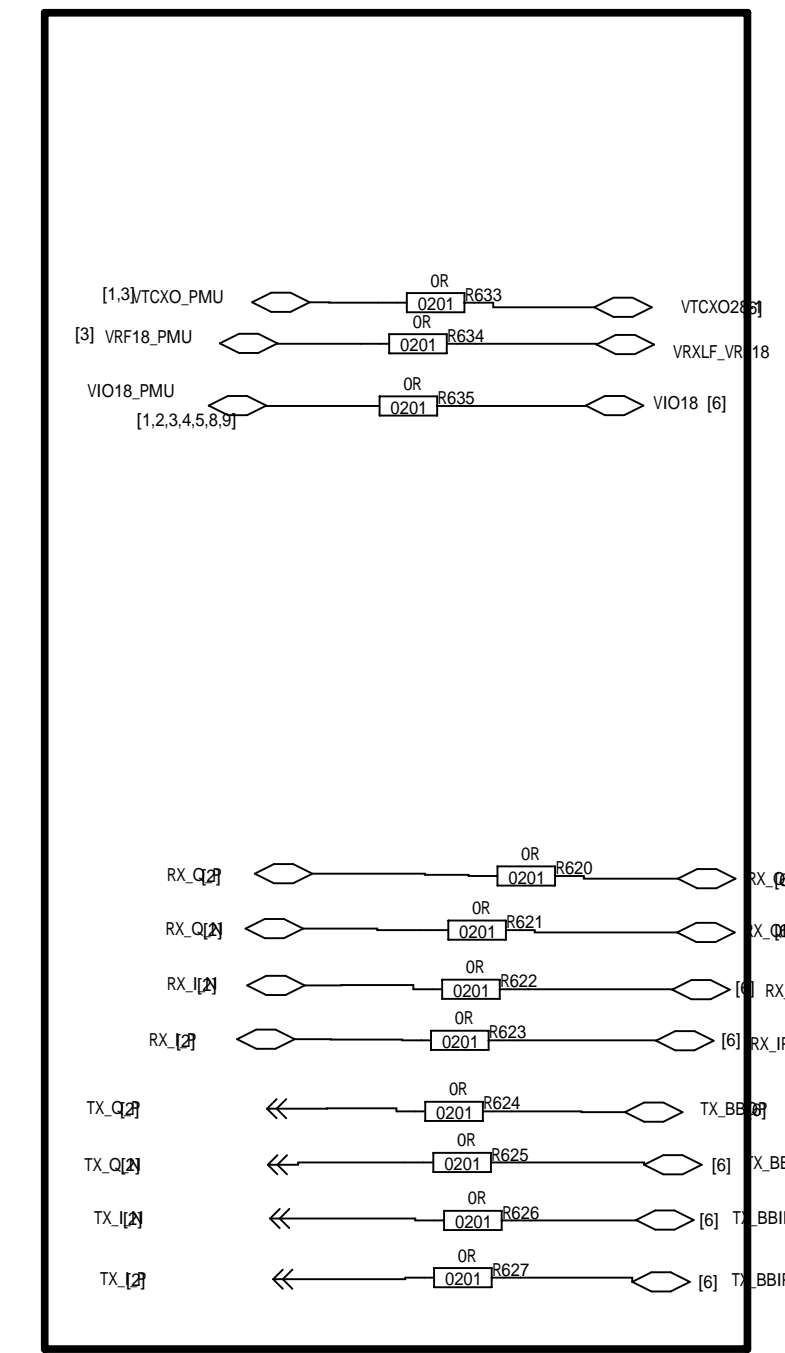
Connect TSX/XTAL GND to GND_AUXADC first than connect to main GND and nearby X600

Route AUXADC_REF/AUXADC_TSX as differential trace with well GND shielding and route AUXADC_GND with 24mil trace width under AUXADC_TSX/AUXADC_REF trace to provide return current path.

MODE	Logic	DCXO_32K_EN	XMODE	VXODIG
DCXO + 32K_XO	(GND)	1(VIC18)	1(VIC18)	1(VIC18)
DCXO + 32K-Less	1(VIC18)	1(VIC18)	1(VIC18)	1(VIC18)

change by summing from MTK 130220

change by summing from MTK 130220



connect to main GND

connect to main GND

connect to main GND

connect to main GND

connect to main GND

connect to main GND

connect to main GND

connect to main GND

connect to main GND

connect to main GND

connect to main GND

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connect to main GND

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connect to main GND

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connect to main GND

connect to main GND

connect to main GND

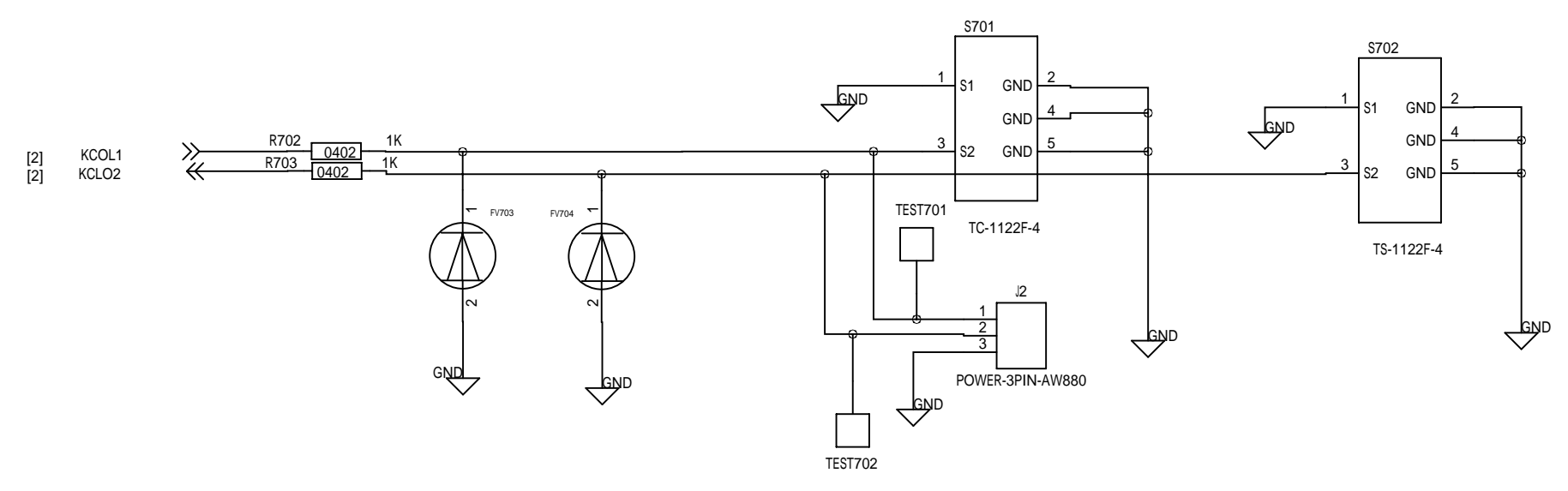
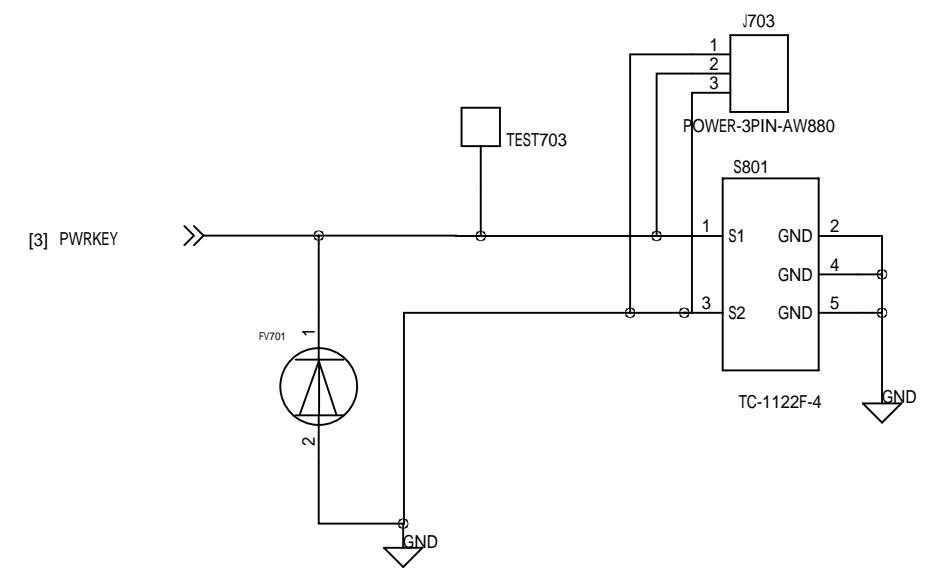
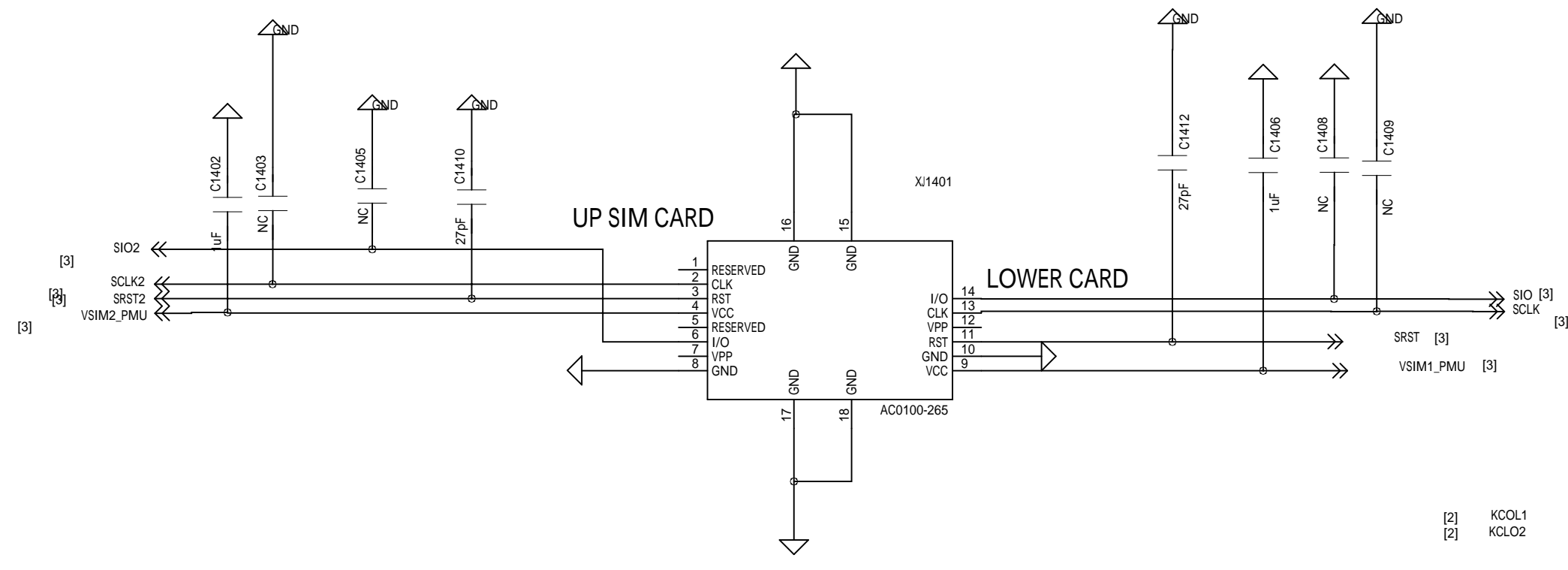
connect to main GND

connect to main GND

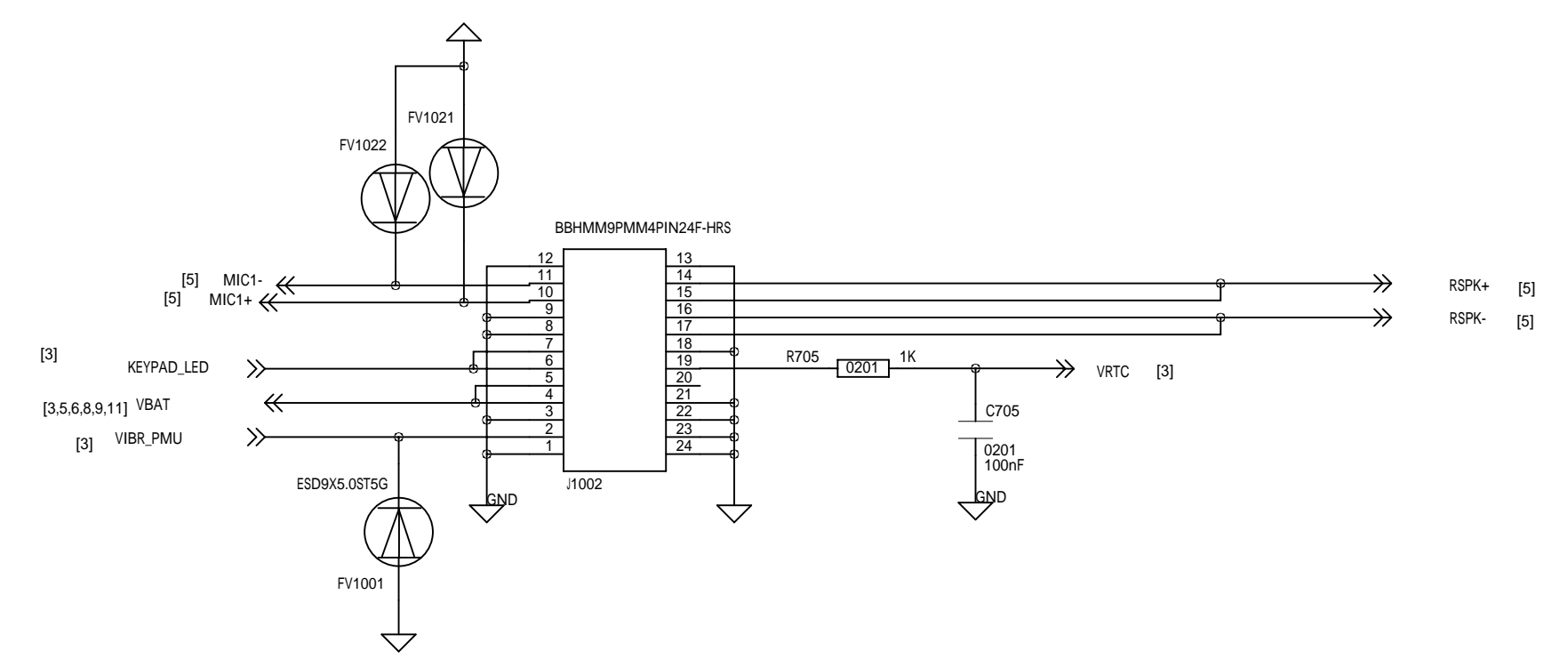
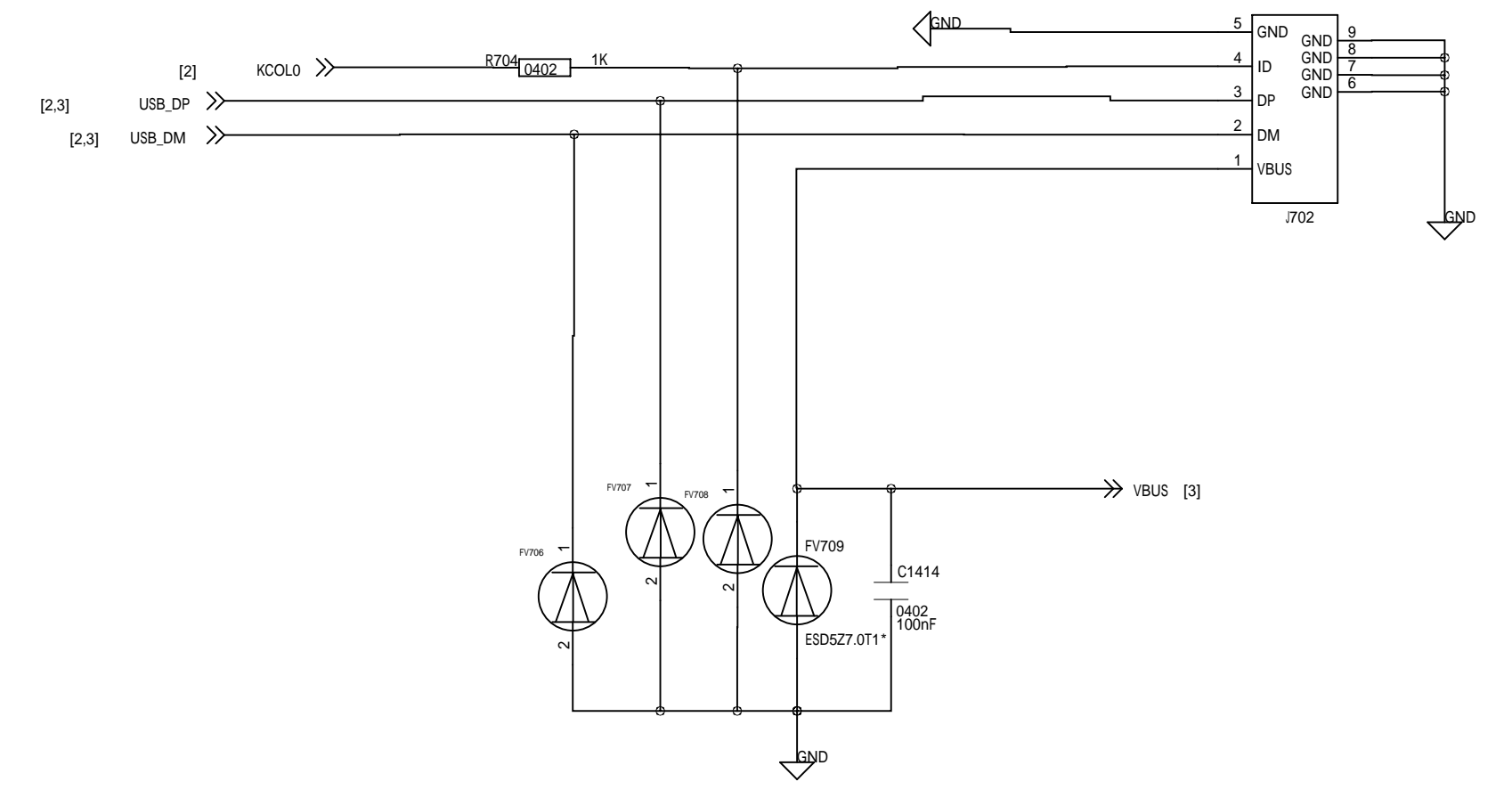
connect to main GND

connect to main GND

SIM CARD

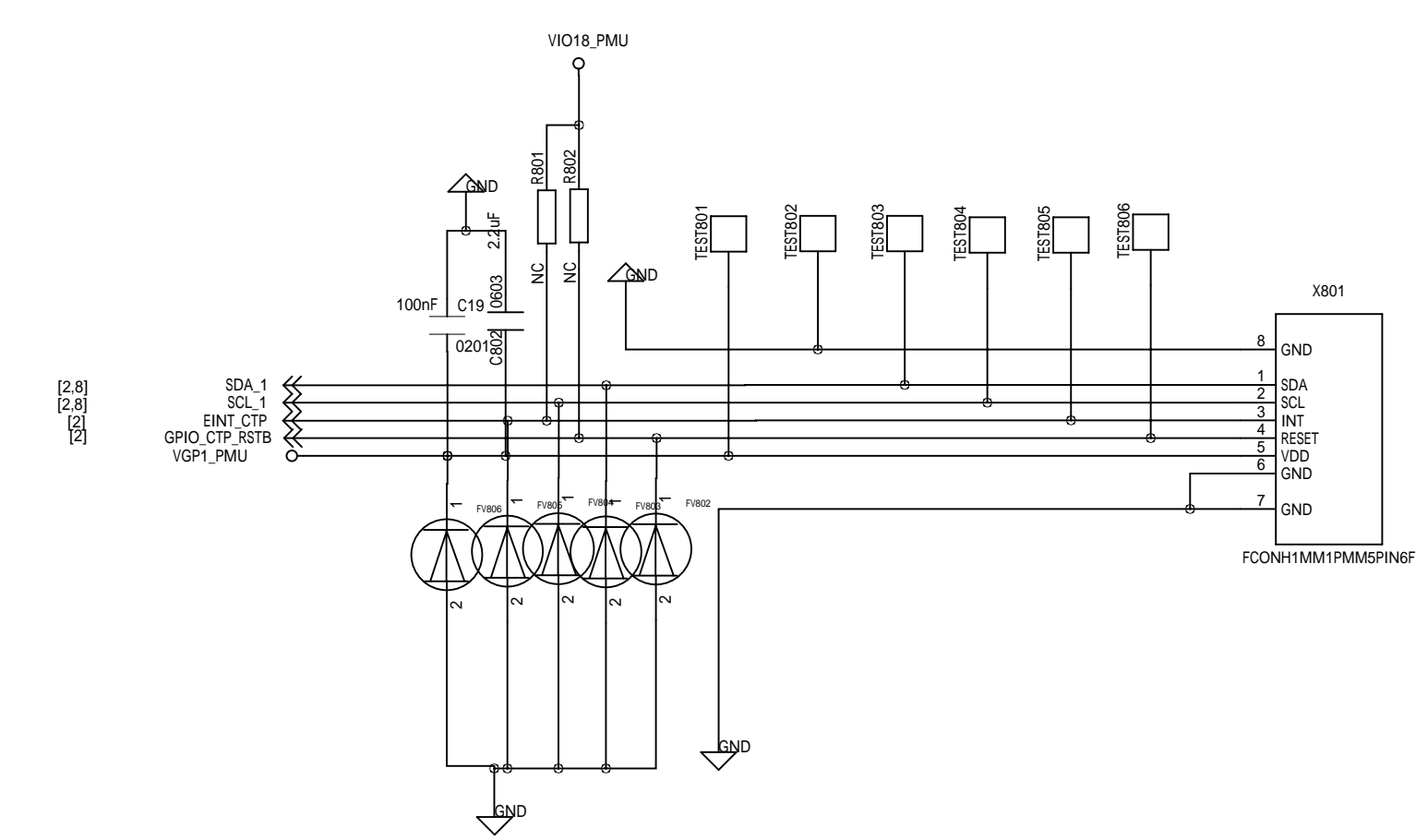
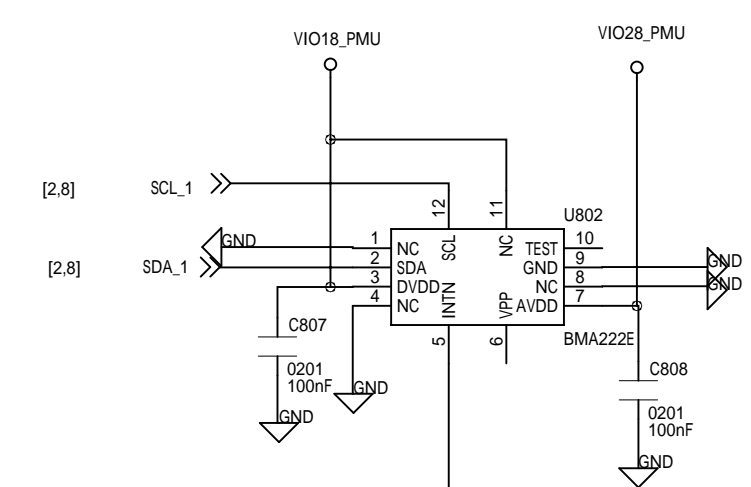
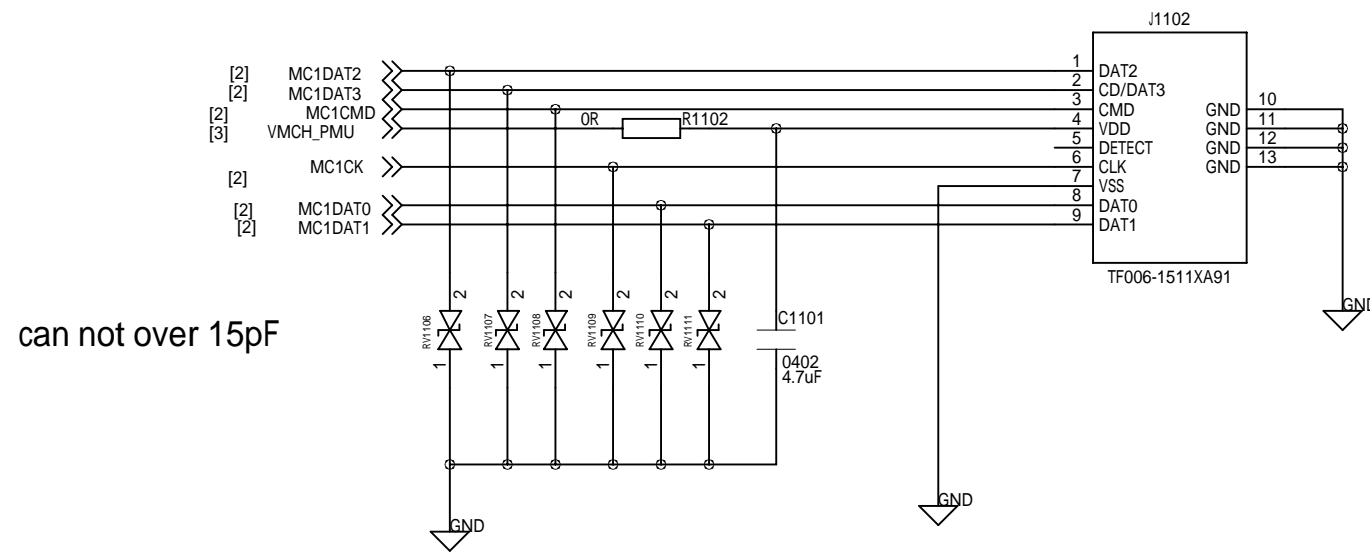


USB HS IF

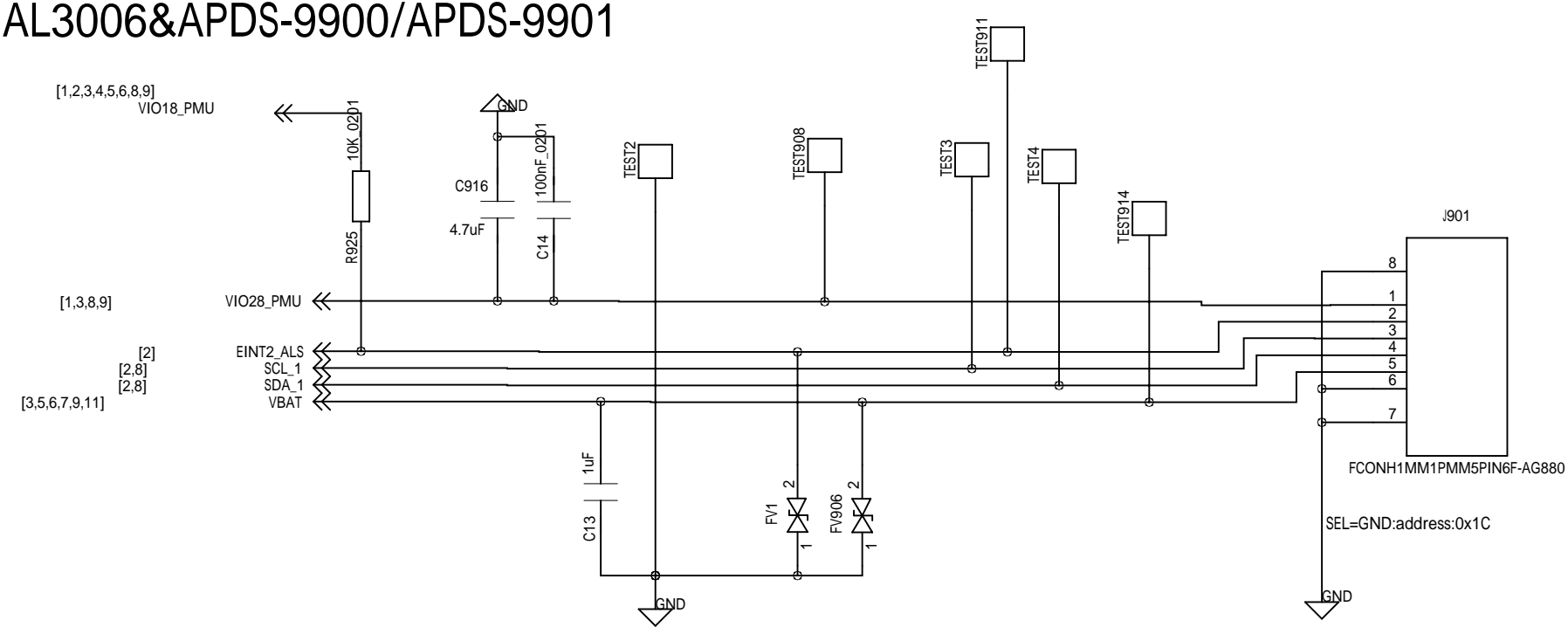


REVISION RECORD			
LTR	ECD NO.	APPROVED:	DATE:

Capacitive Touch Panel



ALS & PS Sensor AL3006&APDS-9900/APDS-9901

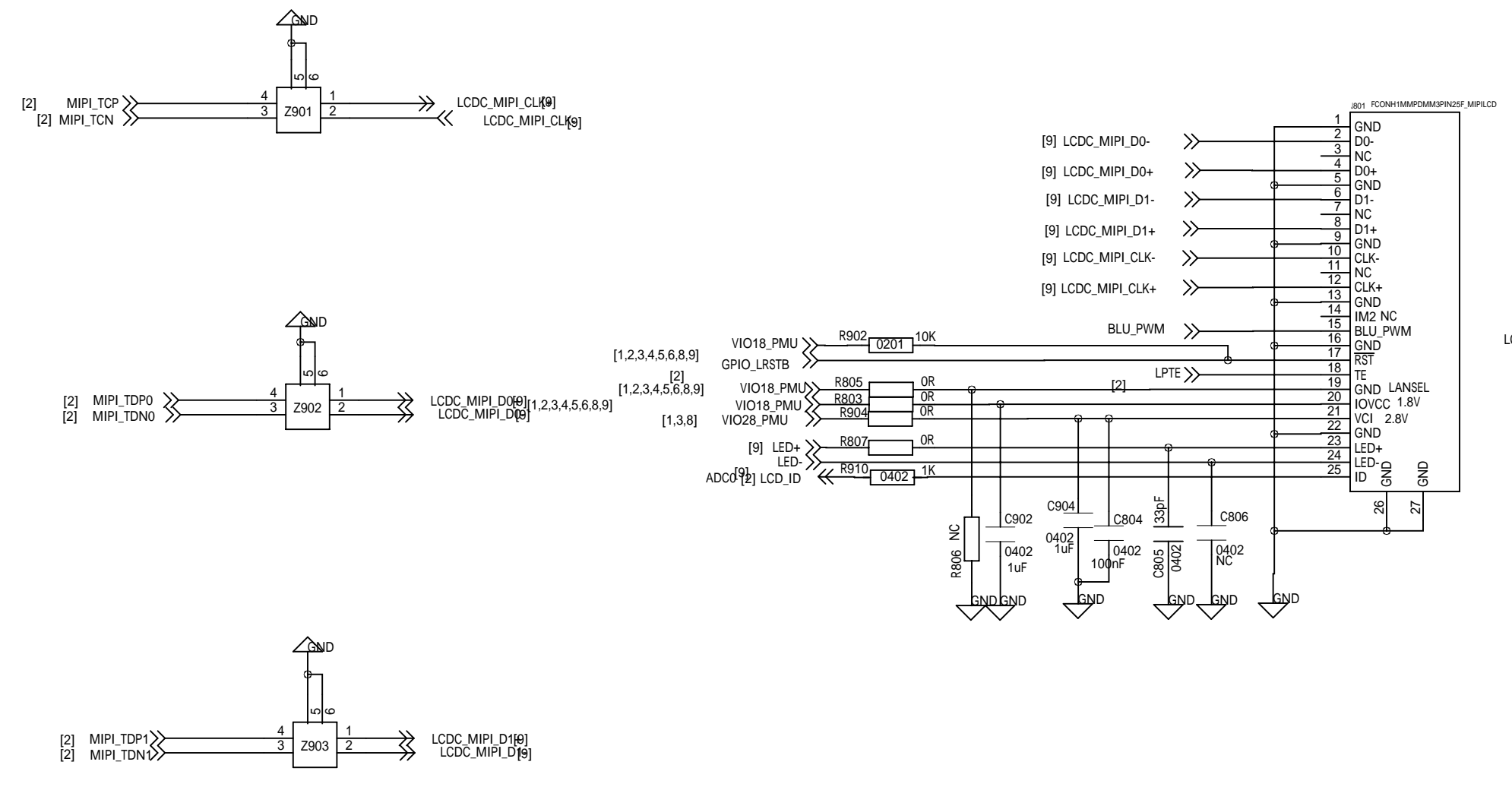


DRAWN:		DATED:		COMPANY: <Company Name>			
CHECKED:		DATED:		TITLE:			
QUALITY CONTROL:		DATED:		CODE: <Code>	SIZE: D	DRAWING NO: 08_SENSOR_CTP	REV: <Revision>
RELEASED:		DATED:		SCALE: <Scale>			
				SHEET: 08 12			

REVISION RECORD			
LTR	ECCO NO.	APPROVED:	DATE

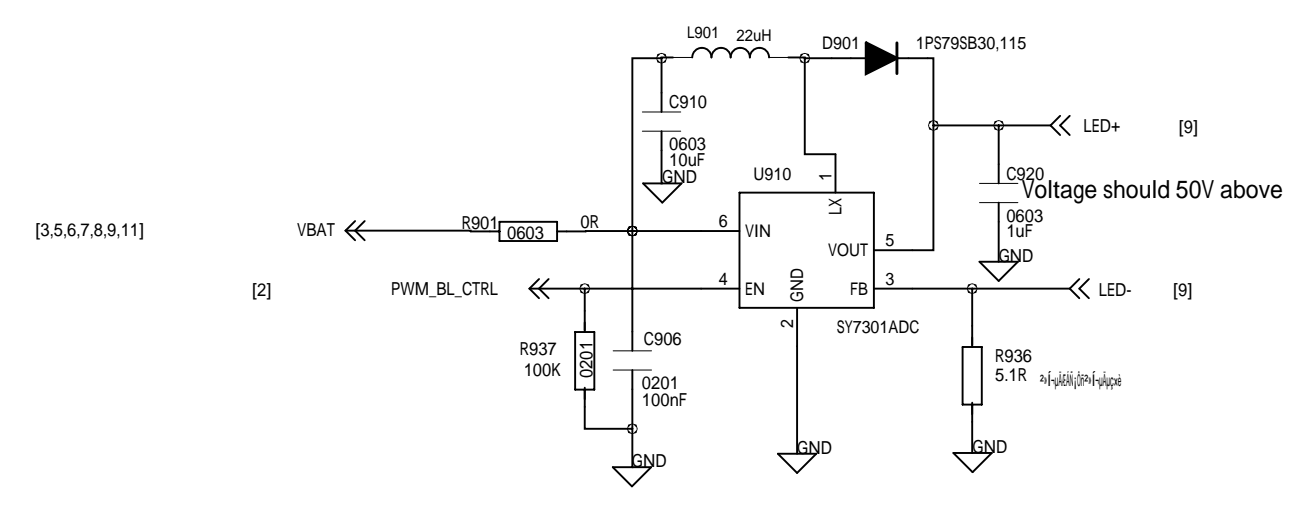
LCM Connector

VLED_P up to 40V
8 LCM BL LED

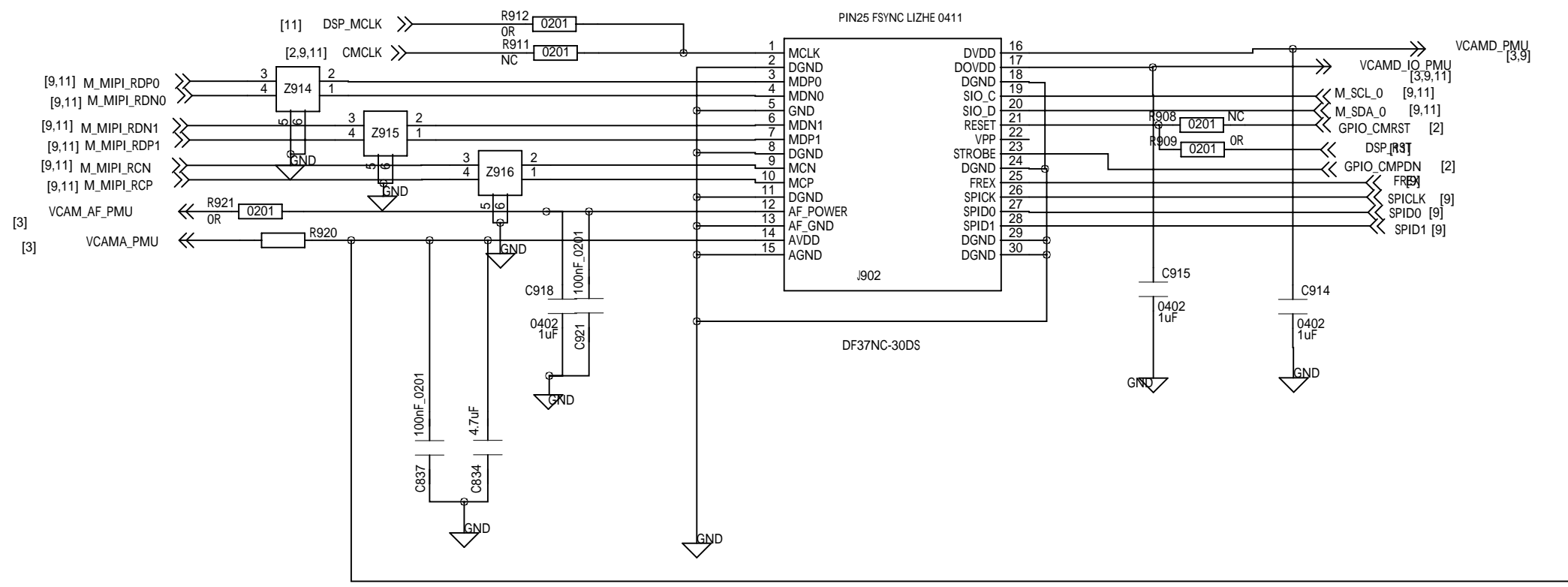
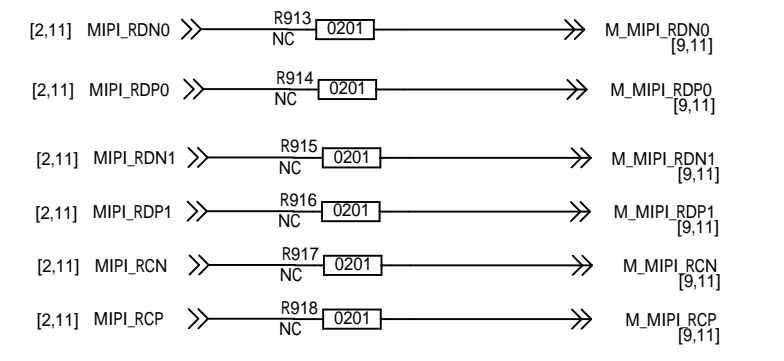


Tianma driver IC=ILI9806C.ID ADC=7V.ID domain=7V

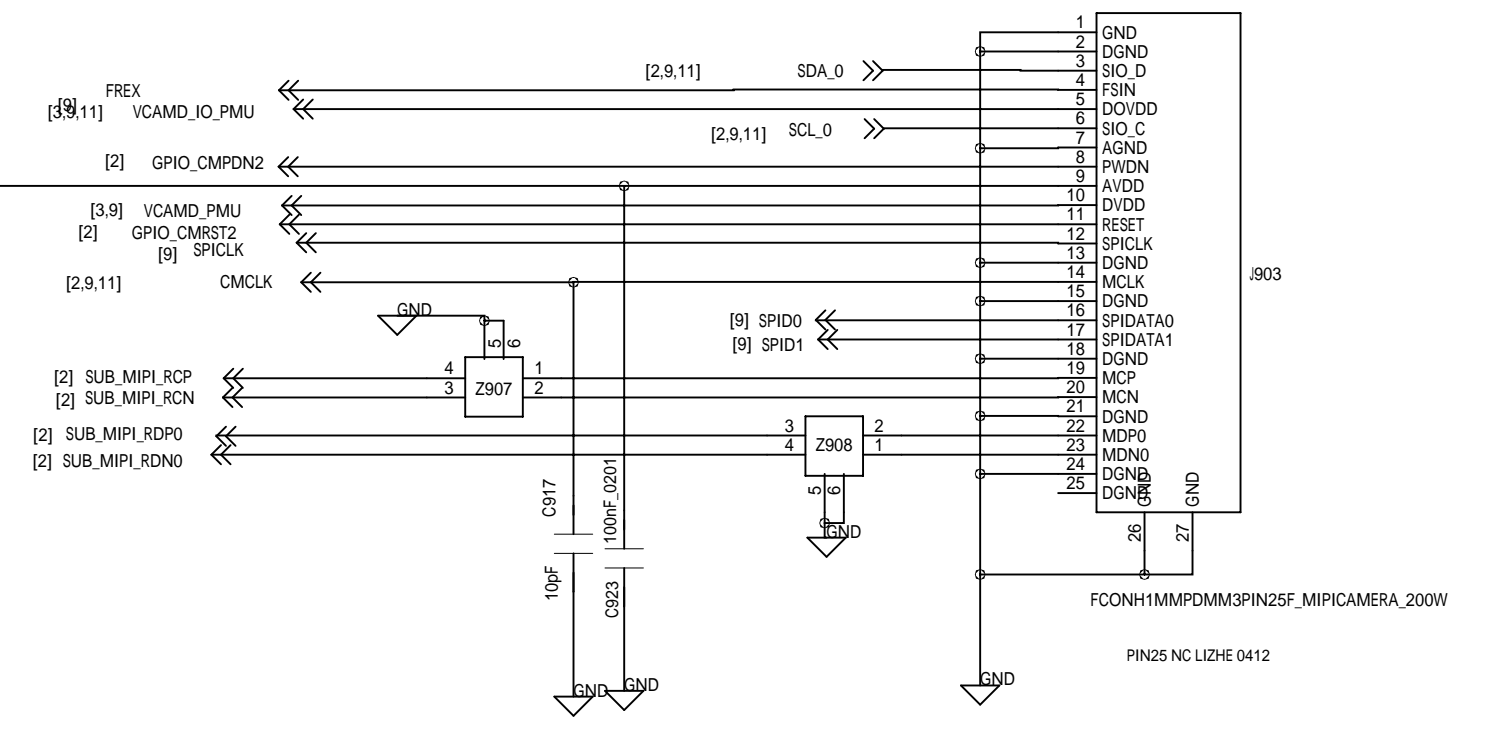
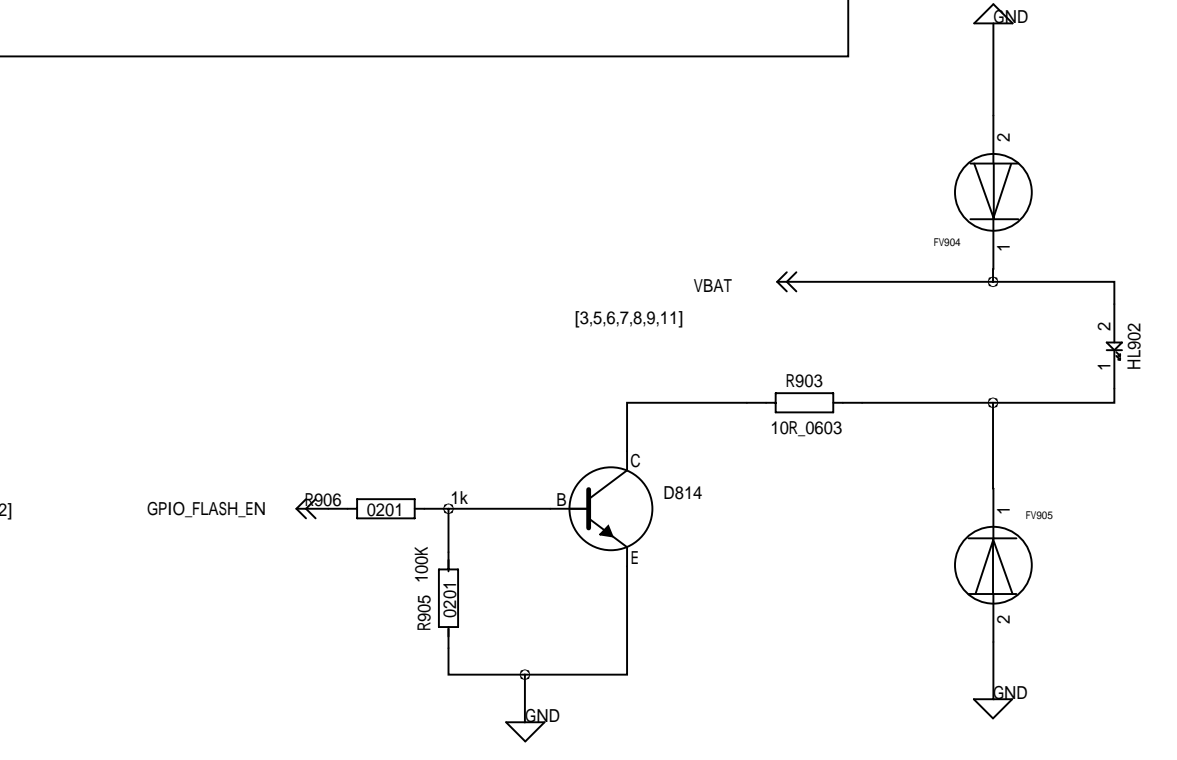
LANSEL
LANSEL=GND,one lane
LANSEL=IOVCC,two lane
IM2????
LCM will make VDD28 power noise



sub Camera



Camera FlashLED.



DRAWN:	DATED:
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

COMPANY: <Company Name>

TITLE:

CODE:	SIZE:	DRAWING NO.:	REV:
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SCALE: <Scale>

SHEET: 8 12

