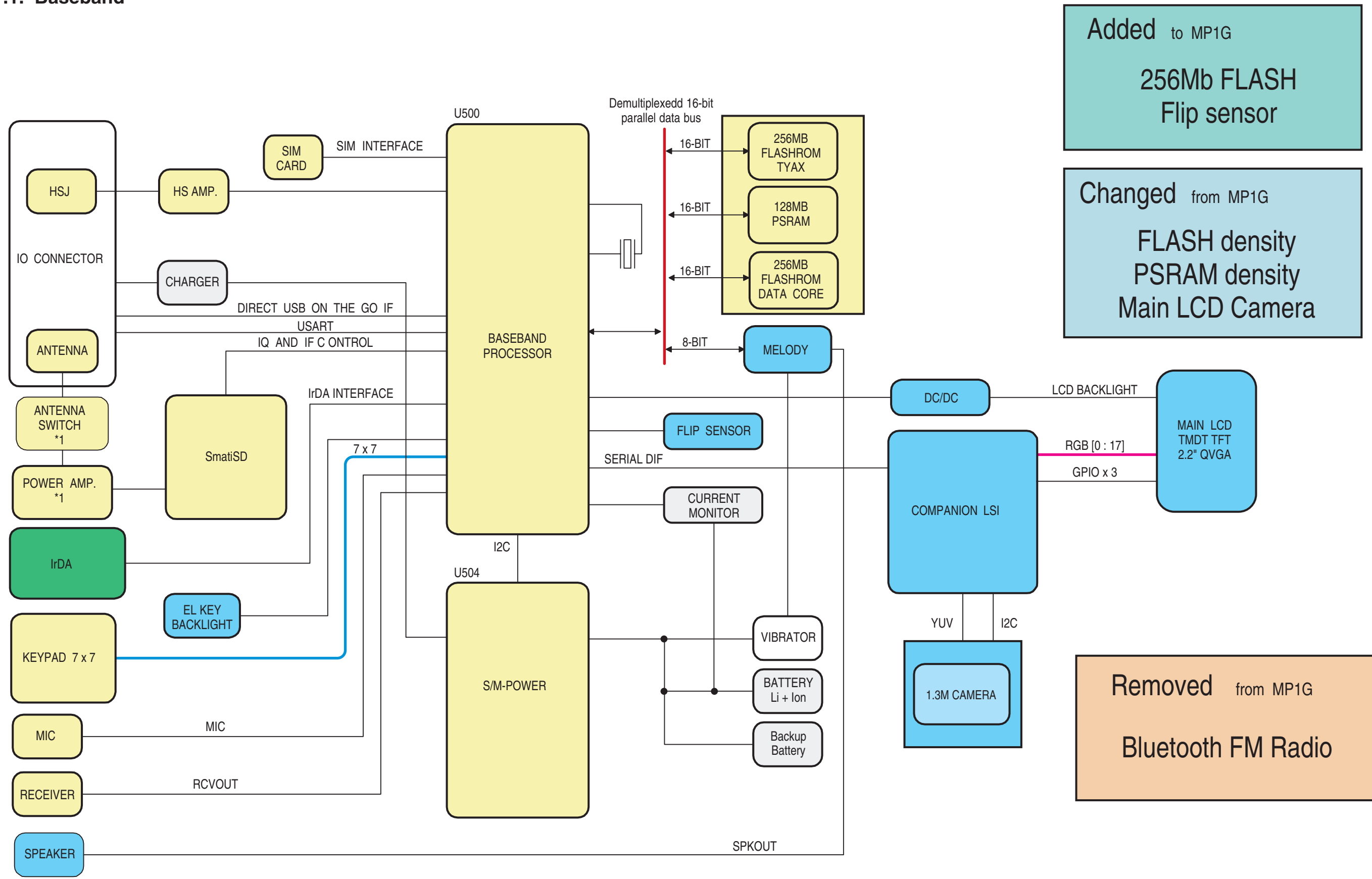


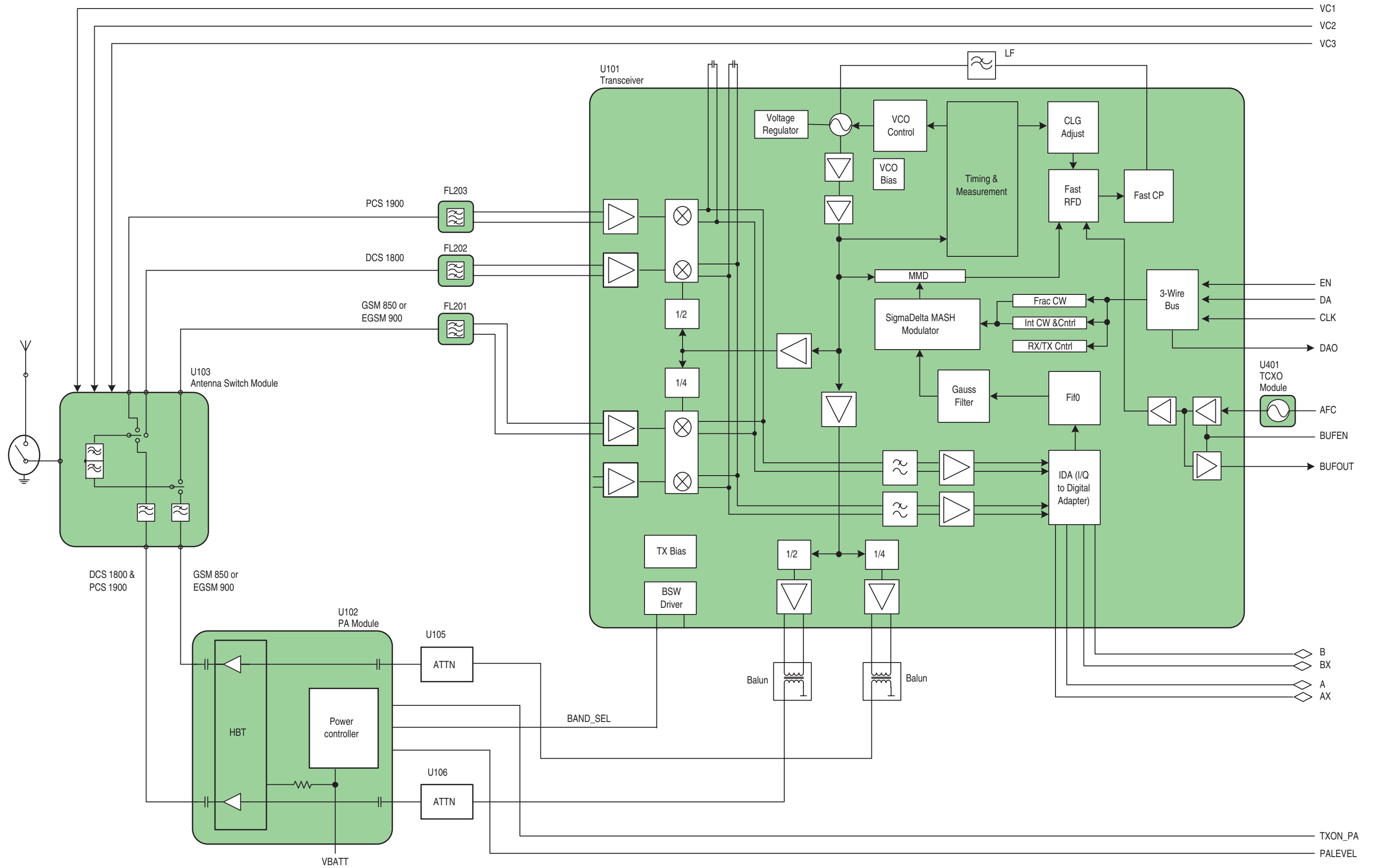
11. BLOCK DIAGRAMS

11.1. Baseband



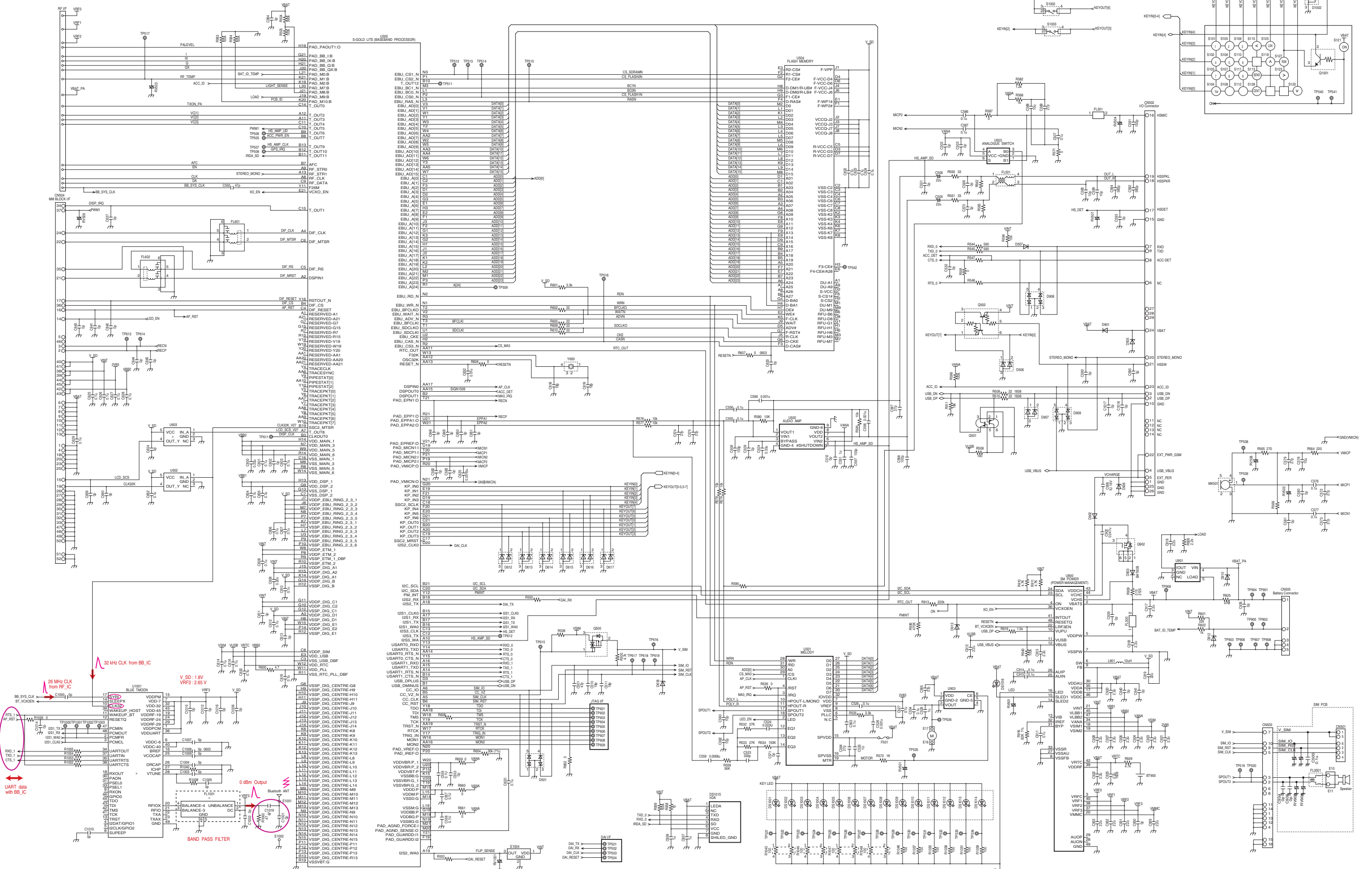
*1 Please refer to Difference table.

11.2. RF Band

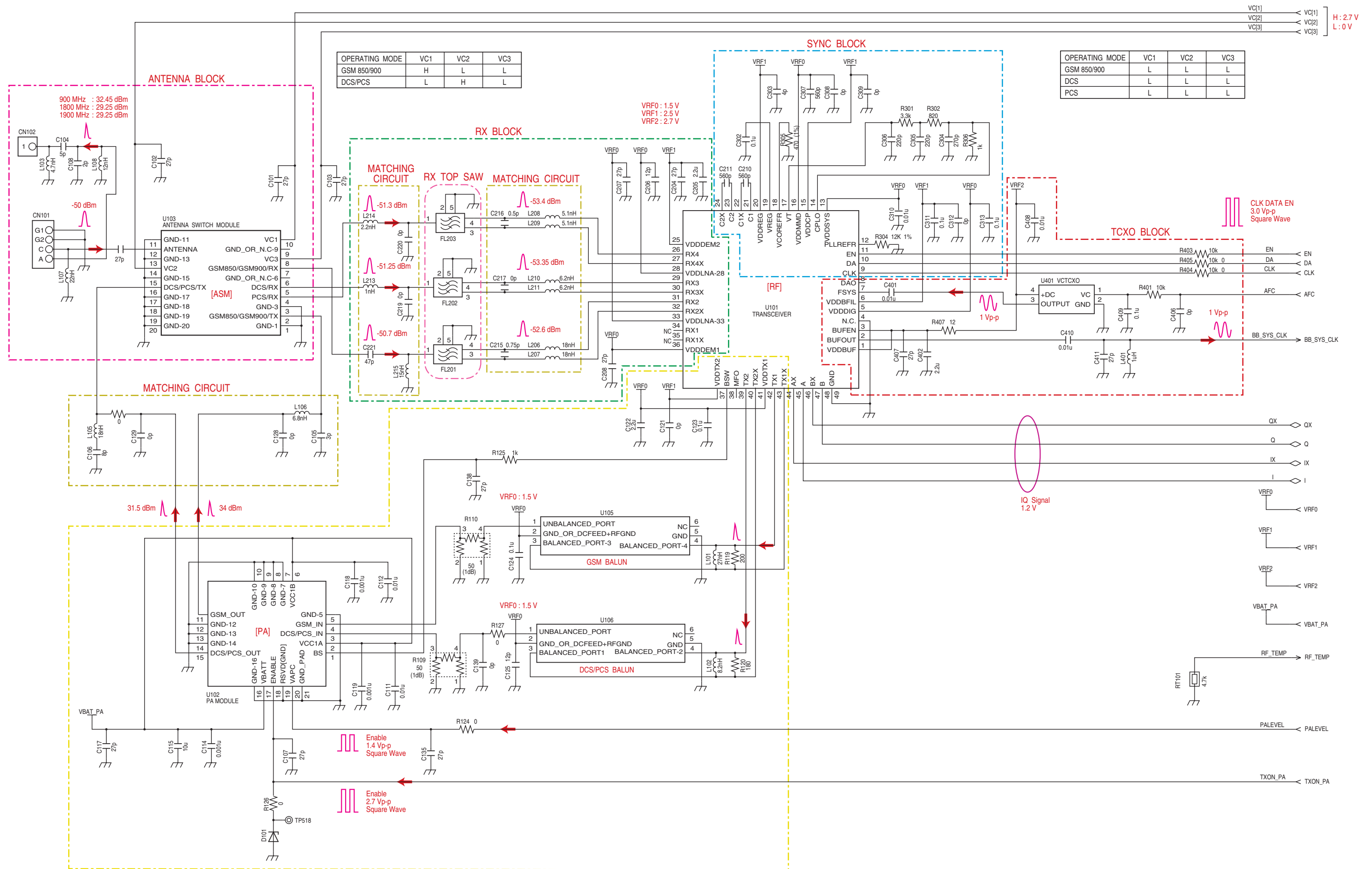


12. CIRCUIT DIAGRAMS

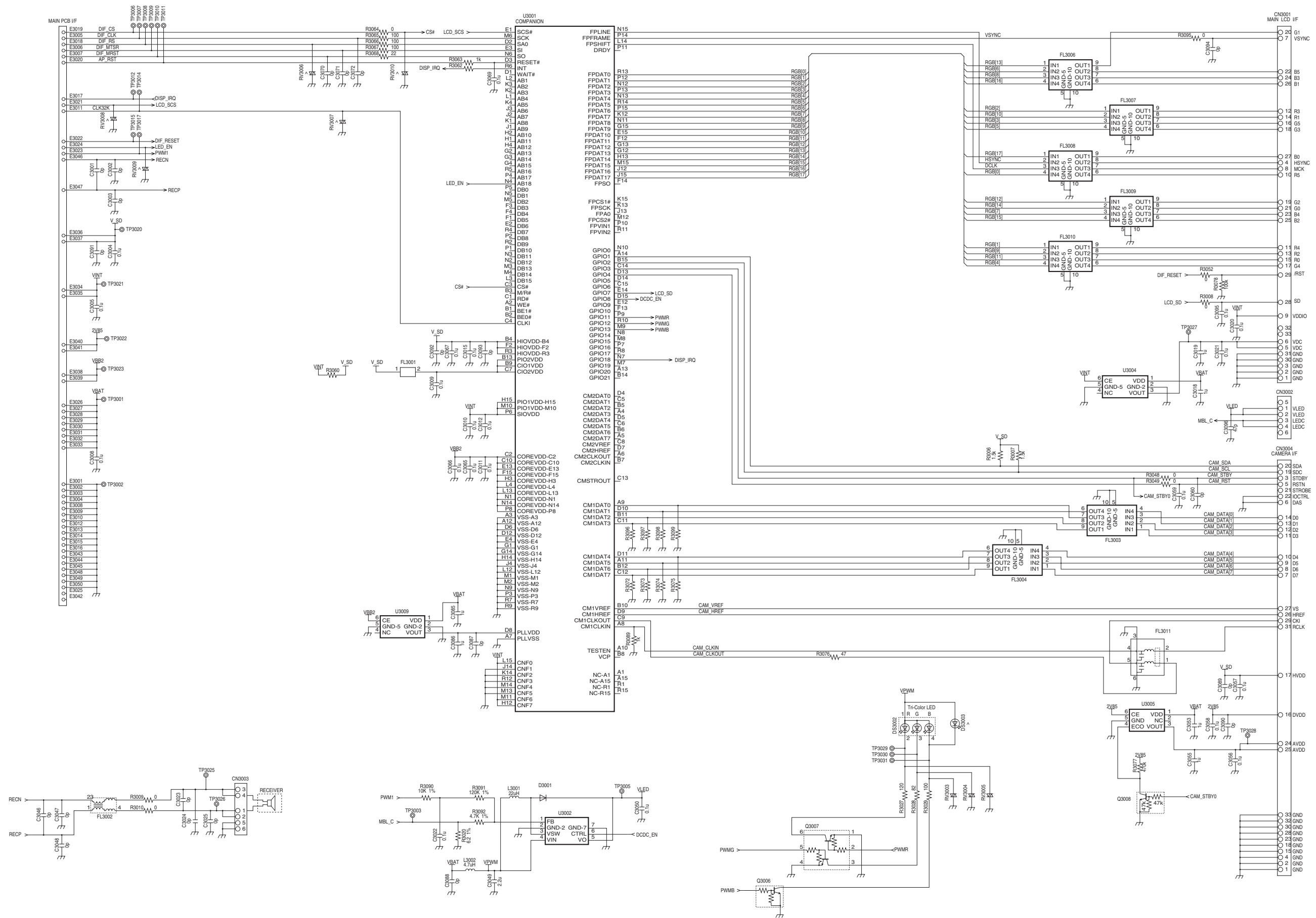
12.1. Main PCB (Baseband)



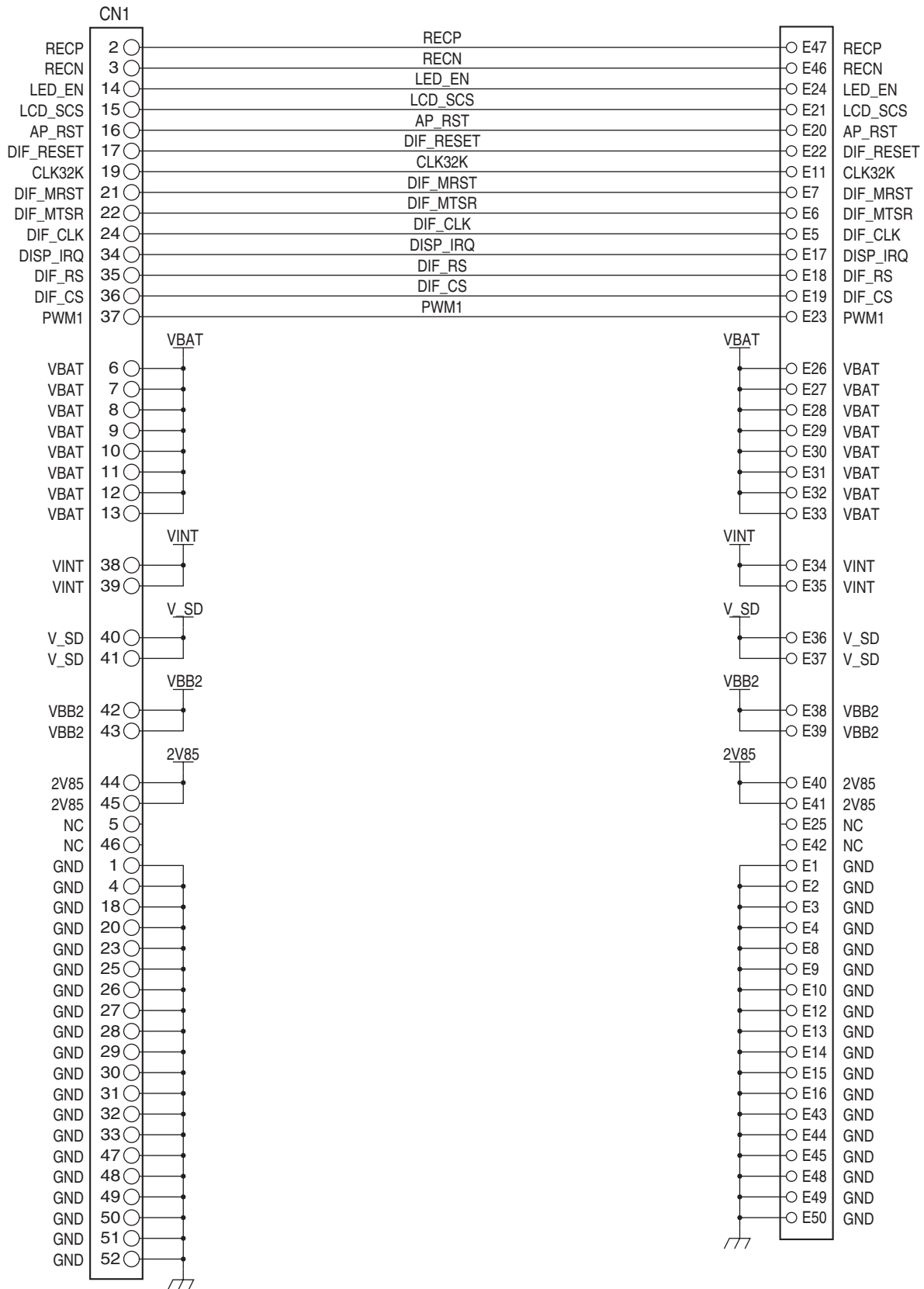
12.2. Main PCB (RF Band)



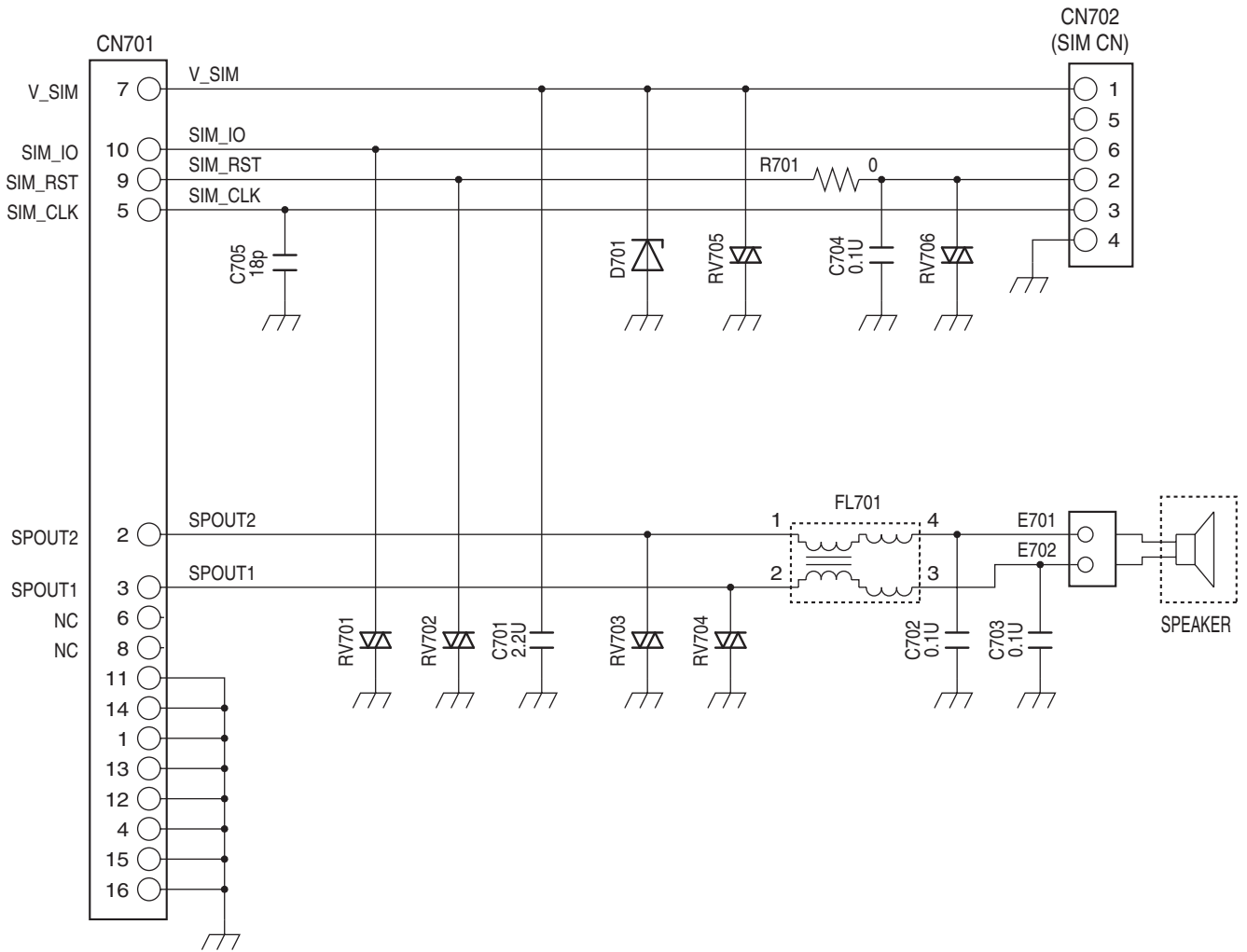
12.3. Upper PCB



12.4. FPC PCB

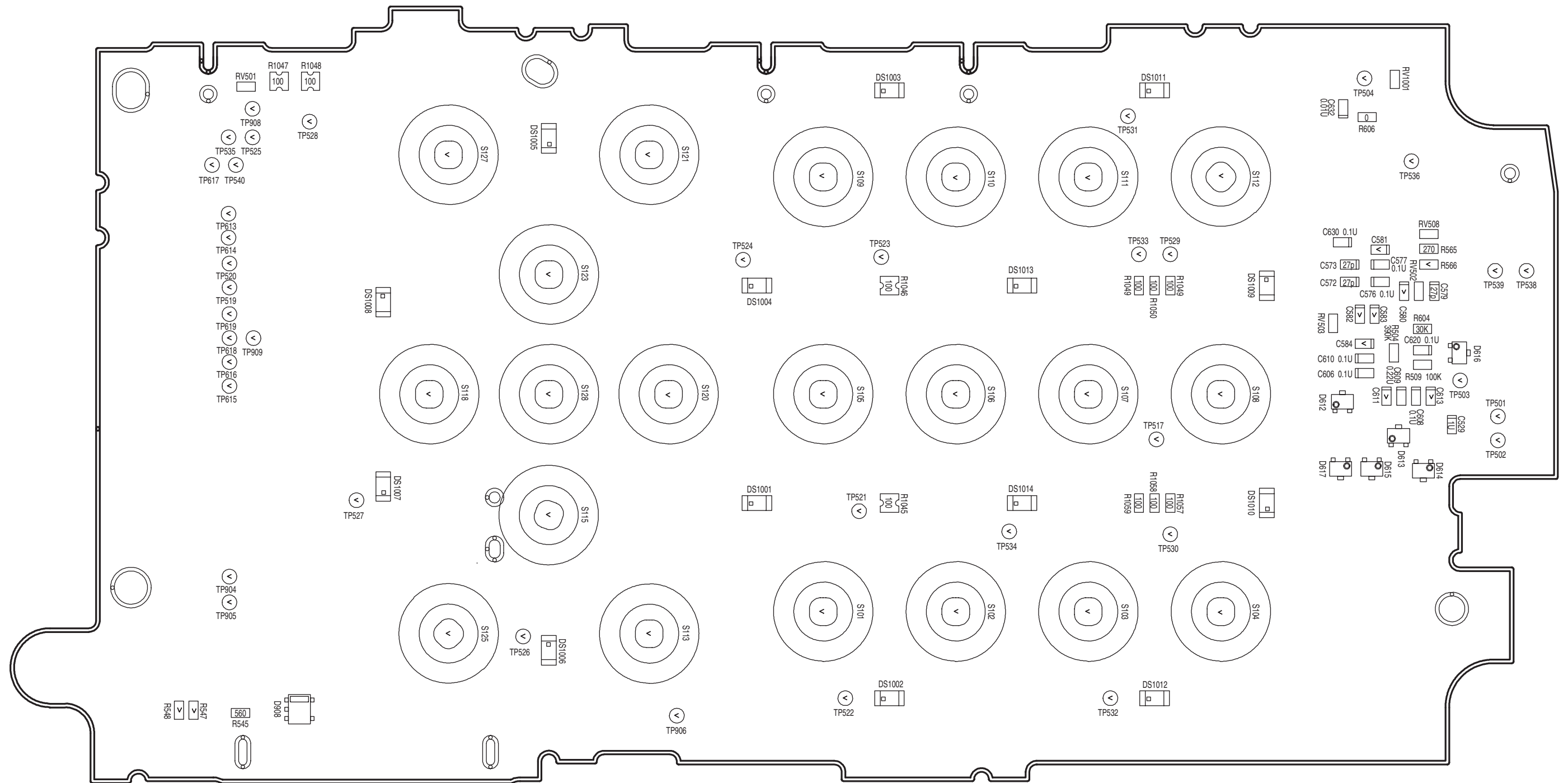


12.5. SIM PCB

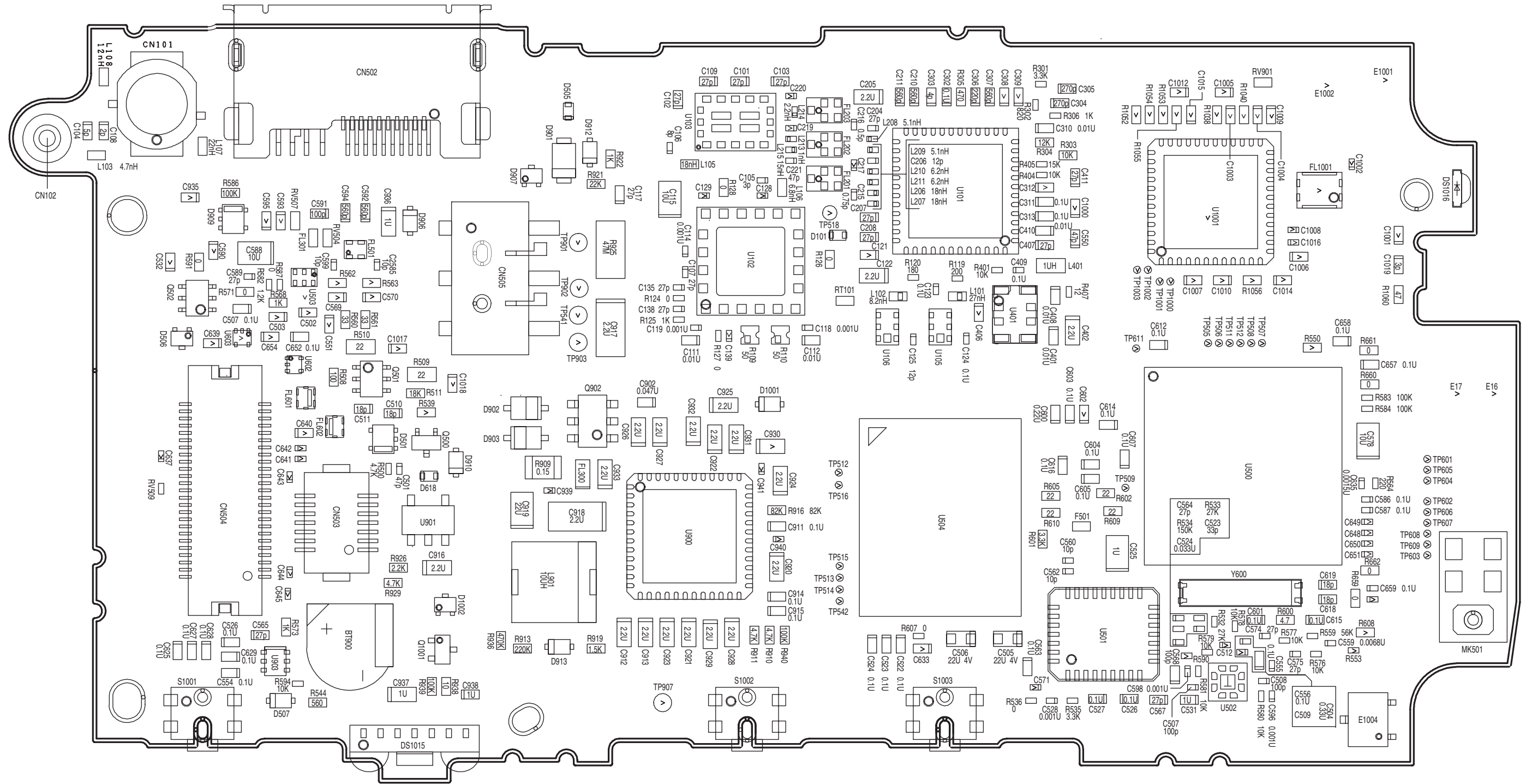


13. LAYOUT DIAGRAMS

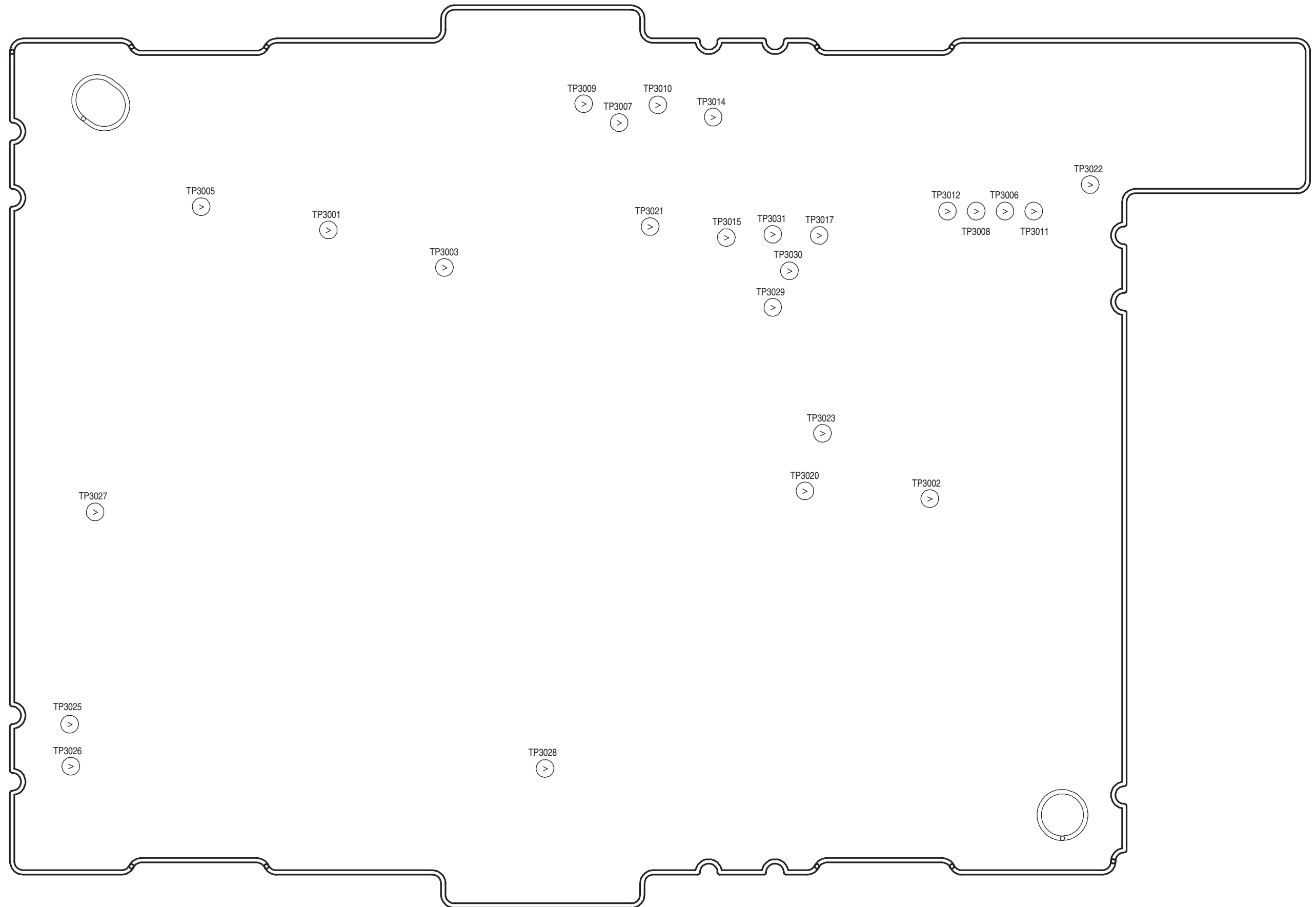
13.1. Main PCB (Upper Side)



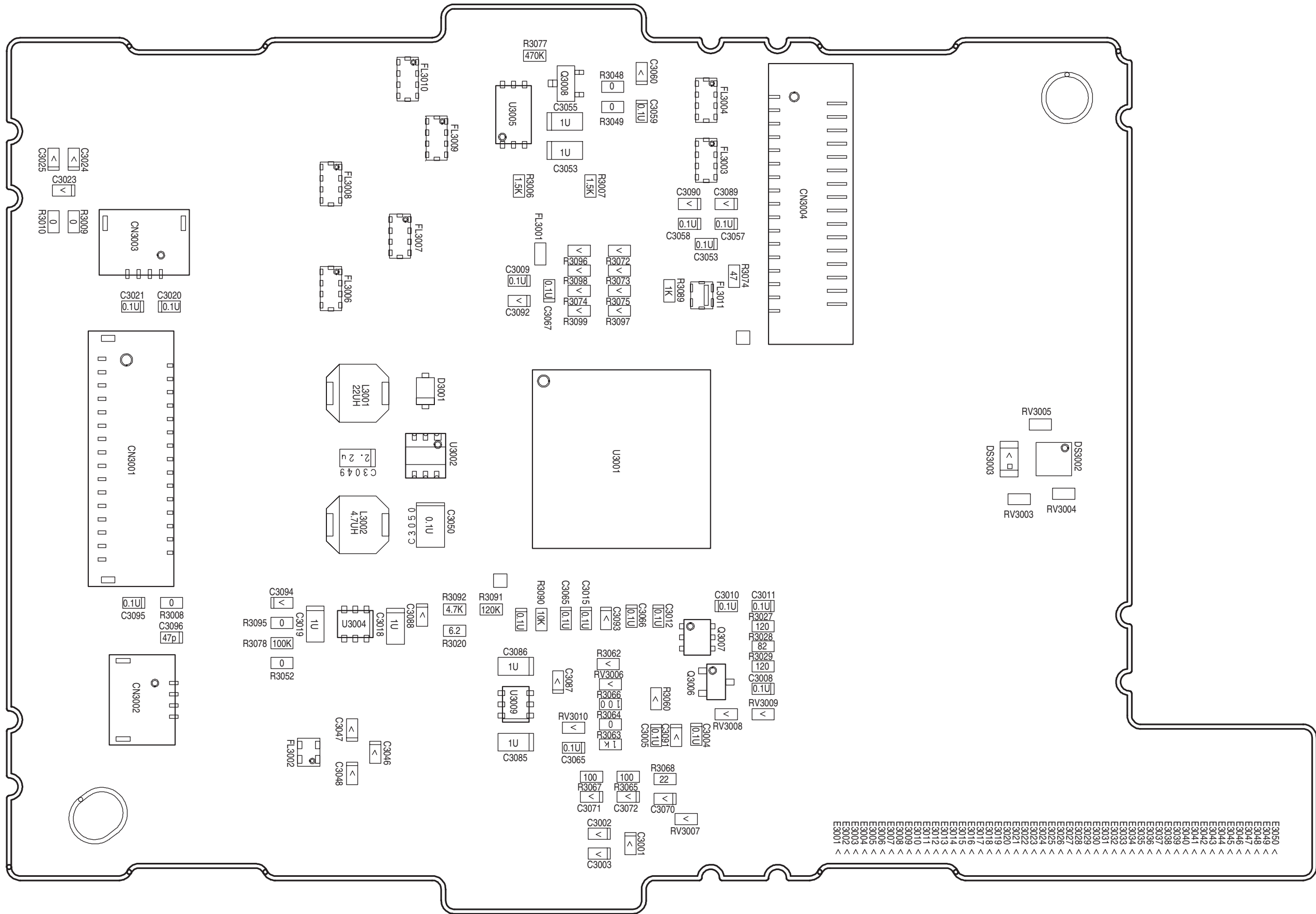
13.2. Main PCB (Bottom Side)



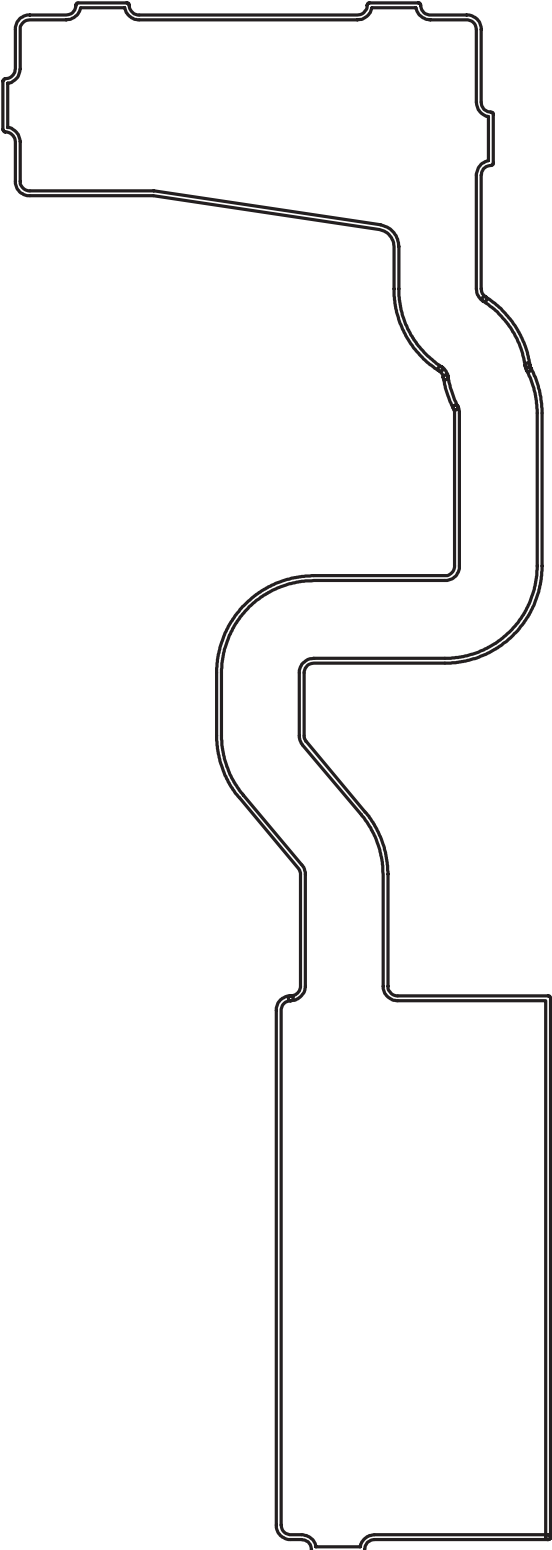
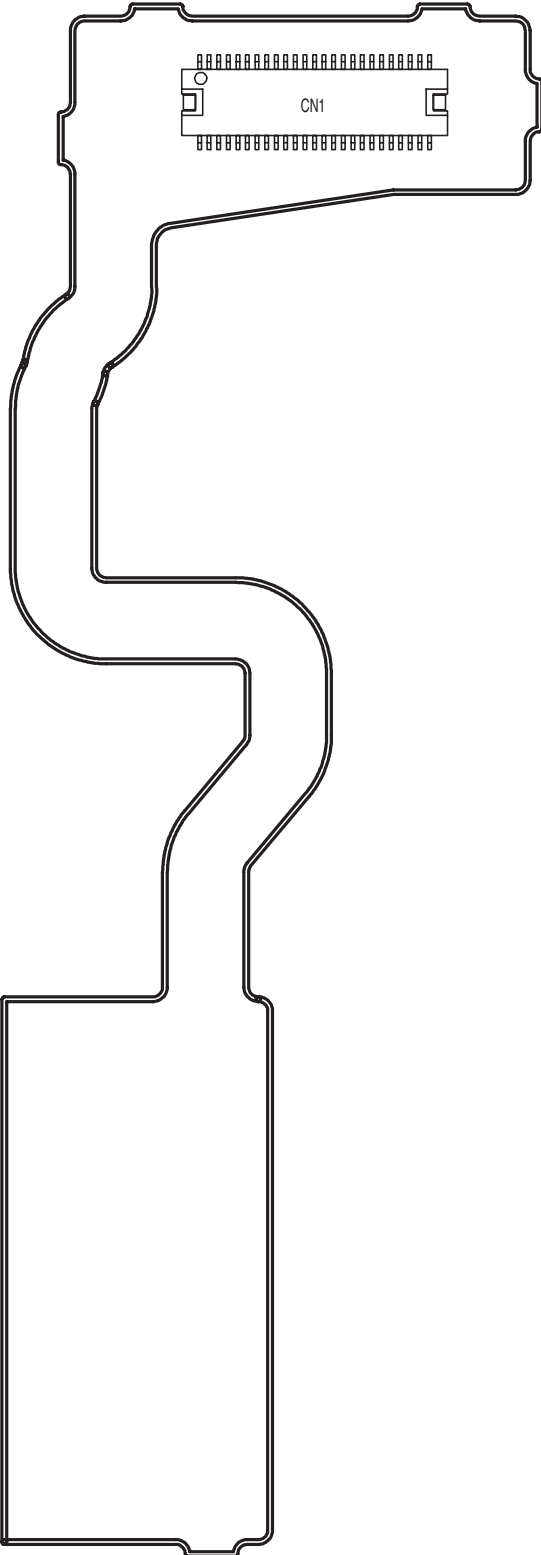
13.3. Upper PCB (Upper Side)



13.4. Upper PCB (Bottom Side)



13.5. FPC PCB



13.6. SIM PCB

